

(19)

Europäisches Patentamt

European Patent Office

Office européen des brevets



(11)

EP 0 810 577 B1

(12)

EUROPEAN PATENT SPECIFICATION

(45) Date of publication and mention
of the grant of the patent:
16.07.2003 Bulletin 2003/29

(51) Int Cl.⁷: **G09G 3/28**(21) Application number: **96305702.1**(22) Date of filing: **01.08.1996****(54) Method of operating a plasma display panel and a plasma display device using such a method**

Verfahren und Einrichtung zum Steuern einer Plasmaanzeigeeinrichtung

Méthode et dispositif de commande d'un panneau à plasma

(84) Designated Contracting States:
DE FR GB

(30) Priority: **17.05.1996 JP 12365896**

(43) Date of publication of application:
03.12.1997 Bulletin 1997/49

(60) Divisional application:
03005606.3

(73) Proprietor: **FUJITSU LIMITED**
Kawasaki-shi, Kanagawa 211-8588 (JP)

(72) Inventors:

- **Kanazawa, Yoshikazu**
Kawasaki-shi, Kanagawa 211 (JP)
- **Kishi, Tomokatsu**
Kawasaki-shi, Kanagawa 211 (JP)

(74) Representative: **Godsill, John Kenneth et al**

Haseltine Lake & Co.,
Imperial House,
15-19 Kingsway
London WC2B 6UD (GB)

(56) References cited:

EP-A- 0 657 861

Note: Within nine months from the publication of the mention of the grant of the European patent, any person may give notice to the European Patent Office of opposition to the European patent granted. Notice of opposition shall be filed in a written reasoned statement. It shall not be deemed to have been filed until the opposition fee has been paid. (Art. 99(1) European Patent Convention).

Description**BACKGROUND OF THE INVENTION****1. Field of the invention**

[0001] The present invention generally relates to a method of operating a plasma display panel and a display device using such a method. More particularly, the present invention relates to a method of operating a plasma display panel constructed by a set of cells (display elements) each having a memory function, and a plasma display device using such a method. Specifically, the present invention concerns an operating method for writing display data in an AC plasma display panel, and a plasma display device in which such an operating method is used.

[0002] In an AC plasma display panel, an alternating voltage is applied between two sustain electrodes so that a discharge is sustained and illuminated display is effected. A cycle of discharge ends 1- to 10- μ s after a pulse is applied. Ions (positive charges) created by the discharge are collected on the surface of an insulating layer on the electrode to which a negative voltage is applied. Electrons (negative charges) are collected on the surface of an insulating layer on the electrode to which a positive voltage is applied.

[0003] After a pulse (write pulse) having a relatively high voltage (write voltage) is applied so that a write discharge is performed and wall charges are formed, a pulse (sustain pulse) having an opposite polarity and having a relatively low voltage (sustain discharge voltage) is applied. Charges created by applying the sustain pulse are superimposed on the wall charges. As a result, the voltage with respect to the ambient space grows to exceed a threshold voltage so that a discharge occurs. To summarize the above, once the write discharge is performed so that the wall charges are created, the discharge is sustained by applying alternating sustain pulses. This phenomenon is referred to as a memory effect or a memory function. In general, the AC plasma display panel displays by utilizing the memory effect.

[0004] A cell in which a discharge takes place is separated from the adjacent cells by ribs or barriers. Ribs or barriers may be provided to surround on all four sides a cell in which a discharge takes place. Alternatively, a rib or a barrier may be provided to cover one of the four sides of the cell so that, on the remaining three sides, the cell is separated from the adjacent cells by optimizing gaps between electrodes.

[0005] The present invention provides a surface-discharge AC plasma display panel using three electrodes in a cell. The technology provided by the present invention is most suitably used when the write discharge (address discharge) for selection of a cell in correspondence with display data is performed in a panel constructed such that a barrier is provided to cover only one of the four sides of the cell. The technology described

hereinafter is particularly useful to advance the development of high-brightness, high-precision and large-scale display panel.

5 2. Description of the related art

[0006] Two types of conventional AC plasma display panels are known: a dual-electrode plasma display panel in which two electrodes are used to perform an address discharge and a sustain discharge; and a triple-electrode plasma display panel in which three electrodes are used to perform the address discharge. In a dual-electrode plasma display panel used as a color display panel capable of display gradations, a fluorescent body (phosphor) formed in a cell is excited by an ultraviolet ray created by the discharge. Since positive ions generated in the discharge directly impinges upon the fluorescent body susceptible to impact of positive ions, the life of the fluorescent body is relatively rapidly exhausted.

[0007] For this reason, a triple-electrode surface-discharge AC plasma display panel is normally used as a color display panel. A triple-electrode surface-discharge AC plasma display panel may be constructed such that a third electrode is formed on the same substrate on which first and second electrodes selected for the sustain discharge are formed. Alternatively, the triple-electrode surface-discharge AC display panel may be constructed such that a third electrode is formed on a separate substrate facing the substrate on which the first and second electrodes are formed.

[0008] The plasma display panel in which the three electrodes are formed on the same substrate may be constructed such that the third electrode is provided above the two electrodes for the sustain discharge. Alternatively, the third electrode may be formed below the two electrodes for the sustain discharge.

[0009] According to another classification, a plasma display panel may be a transparent plasma display panel constructed such that visible light emitted and transmitted by the fluorescent body is observed by human eyes. A reflection plasma display panel is constructed such that the reflection from the fluorescent body is observed.

[0010] A description will now be given of a conventional reflection triple-electrode surface-discharge AC plasma display panel in which the third electrode is formed on a substrate facing the substrate on which the electrodes for the sustain discharge are formed, ribs are formed only in an orthogonal direction (that is, in a direction perpendicular to the direction in which the sustain electrodes lie and parallel to the direction in which the third electrode lies), and each of the sustain electrodes is formed in part by a transparent electrode.

[0011] Fig. 1 shows such a conventional reflection triple-electrode surface-discharge AC plasma display panel 2. Fig. 2 shows another triple-electrode surface-discharge AC plasma display panel 2 which is an elaboration of the panel of Fig. 1 in that the disposition of the

electrodes is improved so that the capacitance between electrodes is reduced. Fig. 3 is a sectional view of the triple-electrode surface-discharge AC plasma display panel of Figs. 1 and 2 taken along the direction in which the third electrodes lie. Fig. 4 is a sectional view of the plasma display panel of Figs. 1 and 2 taken along the direction in which the sustain electrodes lie.

[0012] As shown in Figs. 3 and 4, the triple-electrode surface-discharge AC plasma display panel of Figs. 1 and 2 includes two glass substrates (more specifically, a rear glass substrate and a front glass substrate).

[0013] A first electrode 207 (specifically, X electrode) and a second electrode 208 (specifically, Y electrode) are formed in the front glass substrate 205 with a separation of a discharge slit (that is, a gap between the X electrode 207 and the Y electrode 208 set to about 100 μm). A pair formed by the first electrode 207 and the second electrode 208 constitutes a sustain electrode. Each of these electrodes 207, 208 is composed by a transparent electrode 207A and a bus electrode 207B. The transparent electrode 207A lets a reflected beam 207H from a fluorescent body 207 to pass therethrough. The bus electrode 207B is provided to prevent a voltage drop by an electrode resistance. In addition, the electrodes are coated by a dielectric layer 207C and a MgO (magnesium oxide) film 207D is formed on the discharge side as a protective film. Moreover, a third electrode (address electrode) 209 is formed in the second substrate 206 (specifically, the rear glass substrate 206) opposite to front glass substrate 205 so as to be orthogonal to the first electrode 207. Moreover, a barrier 207E is formed between the address electrode 209 protected with a dielectric 207G. A fluorescent body 207F with a red, green, blue luminescence characteristic is formed so as to cover the address electrode 209 between the barriers 207E. The rear glass substrate 206 and the front glass substrate 205 are assembled such that a ridge of the barrier 207E and the MgO film 207D are in close contact with each other. Moreover, when the discharge slit between the first electrode 207 and the second electrode 208 which form the pair is set to 100 μm , a non-discharge slit which is a gap between two adjacent sustain electrodes in the respective display lines is set to 300 μm . The width of the sustain electrode is set to about 250 μm .

[0014] Fig. 5 is a block diagram of a conventional plasma display device 9 where a peripheral circuit to drive the plasma display panel of Fig. 1 and Fig. 2 is provided. An address pulse for the address discharge is applied to the address electrode 209 using an address driver 28 connected to each of address electrode 209 in the plasma display device 9. The address driver 28 is controlled by a control circuit 281. Moreover, the Y electrode 208 is individually connected to a scan driver 27 (Y scan driver 27).

[0015] The Y scan driver 27 is connected to a Y-side common driver 22. The pulse for the address discharge is generated by the scan driver 27. The sustain pulse

etc. are generated by the Y-side common driver 22. These pulses are applied to the Y electrode 208 via the Y scan driver 27. The Y-side common driver 22 is controlled by a common driver control unit 221 provided in a panel operation control unit 281A. The Y scan driver 27 is controlled by a scan driver control unit 271 provided in the panel operation control unit 281A.

[0016] The X electrode 207 is connected together in the entire display lines 201 of a plasma display panel 2.

5 A X-side common driver 22 (not shown) generates the write pulse, the sustain pulse, etc. and is controlled by the common driver control unit 221. The common driver control unit 221, the scan driver control unit 271, and the control circuit 281 are controlled with a vertical sync signal (VSYNC in Fig. 5) and a horizontal sync signal (HSYNC in Fig. 5) input from outside the device to the panel operation control unit 281A, and with a display data signal (DATA in Fig. 5) and a dot clock (CLOCK in Fig. 5) input to a display data control unit 281B. The display data signal DATA input according to the dot clock CLOCK is stored in a frame memory 281B-1.

[0017] Fig. 6 is a waveform chart which shows a conventional method of operating the plasma display panel 2 shown in Figs. 1 - 4 with the circuit shown in Fig. 5. 10 The chart illustrates one sub-field period in the separated address period/sustain discharge period write addressing.

[0018] One sub-field in the conventional method is divided into a reset period, an address period, and a sustain discharge period. All the Y electrodes 208 are first set at a 0 V level and a whole-screen write pulse of a Vs+Vw (specifically, about 300 V) is applied to the X electrodes 207 at the same time for the reset period. The discharge is caused in all cells of all the display lines 201 regardless of the previous state of the display. A potential Vaw of the address electrode 209 at this time is about 100 V. Next, the potential of the X electrode 207 and the address electrode 209 becomes 0 V. In all cells, the voltage due to a wall charge 204 exceeds a discharge-initiating (firing) voltage and the discharge is begun. The space charge is self-neutralized and the discharge ends, since this discharge does not involve the potential difference between the electrodes. That is, a so-called self-erase discharge occurs. All cells in the panel enters a uniform state without the wall charge 204 built up, as a result of this self-erase discharge. The resetting has an action by which all cells are in the same state regardless of the previous state of the sub-field.

30 As a result, it is possible to perform a subsequent address discharge (that is, the writing) in a stable manner. 40 [0019] Next, the line sequential address discharge is caused in the address period according to the display data to control the activating of the cell. Figs. 7A - 7C show the mechanism of this address discharge.

50 [0020] A scan pulse 21 at a -VY level (specifically, about -150 V) is applied to the Y electrode 208. An address pulse of a voltage Va (specifically, about 50 V) is selectively applied to address electrode 209 corre-

sponding to the cell which is activated for illumination, that is the cell which is a target for the sustain discharge. The discharge occurs between the address electrode 209 and the Y electrode 208 of the cell which is lighted (see Fig. 7A). Next, this discharge triggers the discharge between the X electrode 207 and the Y electrode 208 as a priming discharge (see Fig. 7B). As a result, the wall charge 204 of an amount by which the sustain discharge is enabled is collected on the MgO film 207D on the X electrode 207 and the Y electrode 208 of the selected line 202 (see Fig. 7C). A similar operation is executed one by one for the other display lines 201. In all the display lines 201, new display data is written. Afterwards, in the sustain discharge period, the sustain pulse having a voltage of Vs (about 180 V) is alternately applied to the Y electrode 208 and the X electrode 207 so that the sustain discharge is caused. The image of one sub-field field is displayed. In this "separated address period/sustain discharge period write addressing", the duration of the sustain discharge period determines the brightness. That is, the brightness depends on the frequency of the sustain pulse (voltage Vs).

[0021] Fig. 8 is a time chart showing the sequence of the separated address period/sustain discharge period write addressing of Fig. 6.

[0022] In the separated address period/sustain discharge period write addressing, one frame is divided into eight sub-fields SF8, SF1, SF2, SF3, SF4, SF5, SF6, and SF7. In these sub-fields SF1-SF8, the reset period and the address period have the same duration. Moreover, the ratio of the durations of the sustain discharge period is 1: 2: 4: 8: 16: 32: 64: 128. Therefore, by selecting the sub-field to be lighted, it is possible to display the brightness of 256 steps from 0 to 255. That is, a 256-step gradation display is enabled.

[0023] Specifically, one frame has the duration of 16.6 ms (1/60 Hz) assuming that the cycle of rewriting the screen is 60 Hz. Moreover, when the pulse frequency in one frame of the sustain discharge (referred to as the sustain cycle) is assumed to be 510 times per frame, 2 cycles occur in the sub-field SF1, 4 cycles occur in the sub-field SF2, 8 cycles occur in the sub-field SF3, 16 cycles occur in the sub-field SF4, 32 cycles occur in the sub-field SF5, 64 cycles occur in the sub-field SF6, 128 cycles occur in the sub-field SF7, and 256 cycles occur in the sub-field SF8. When duration of the sustain cycle is assumed to be 8 ms, the total duration in one frame becomes 4.08 ms. About 12 ms of the remainder is allocated for the eight reset periods, address periods, and stop periods. Therefore, the reset period and the address period of each sub-field have the duration of about 1.5 ms. When it is assumed that about 50 ms is necessary for the reset period of each address period, the address cycle becomes 3 ms to drive the panel of 500 lines.

[0024] However, high-brightness, high-resolution, and large scale design can be achieved in the plasma display device 9 which uses the above-described con-

ventional method, by connecting the X electrode 207 with a common bus to provide an easy leading out of the panel electrode to the circuit side and the simplification of the circuit. As a result, though the Y electrode 208 and the address electrode 209 are fed the selection potential or the non-selection potential, no stable operation is enabled because the X electrode 207 is connected to the common bus.

[0025] A further explanation will now be given of the problem in making a high-resolution plasma display device 9 which uses a conventional method of operating a plasma display panel. The explanation will be given based on the construction of the plasma display panel 2 shown in Figs. 1 - 4. It is to be noted that raising the brightness by raising the frequency of lighting has a limitation in terms of the power consumption, the time distribution and the life of the device. Hence, it is necessary to raise the lighting efficiency.

[0026] One method of raising the lighting efficiency is to allow the discharge to be conducted within a wide range and to positively activate the discharge. Narrowing the discharge slit (that is, the gap between the transparent electrode 207A of the X electrode 207 and the Y electrode 208) to only a limited degree and enlarging

the width of the transparent electrode 207A are advantageous to allow the discharge to be conducted within a wide range. Another method is to increase the numerical aperture so that the beam generated in the fluorescent body 207F is led to the surface without much disturbance. In the case of the reflection device, it is desirable that the width of the bus electrode 207B be relatively small because the bus electrode 207B presents an obstruction to the reflected beam 207H. However, the resistance element of the electrode is increased when the width of the bus electrode 207B is narrowed too much, increasing the voltage drop when the discharge current flows. As a result, the voltage applied to the cell decreases, the activation of the discharge is disturbed consequently, decreasing the brightness. Moreover, the amount of the voltage drop depends on the magnitude of a display area. Therefore, a change in the magnitude of the display area brings about a change in the brightness, significantly reducing the display quality occasionally.

[0027] Considering above-mentioned point, it is preferable to enlarge the width of the transparent electrode 207A and to narrow the bus electrode 207B only to a limited degree. As a result, the non-discharge slit on the reverse side with respect to the discharge slit will become narrow under a given size of the cell. When the non-discharge slit is too narrow, the discharge-initiating voltage for the discharge slit and that for the non-discharge slit approaches (the discharge-initiating voltage is determined depending on the product of the distance and the gas pressure between the electrodes as well as on the composition of the enclosed gas, the dielectric substance material, and the quality of MgO film 207D), so that the cells are prevented from being properly sep-

arated from each other. There is known a plasma display panel in which the stripe barrier 207E is formed in the non-discharge slit so as to separate the cells (that is, the discharge space) properly.

[0028] Providing the stripe barrier 207E in the non-discharge slit prevents making of a plasma display panel 2 of high resolution and will make it difficult to manufacture the plasma display panel 2 with precision. The barrier 207E is often formed with the thick-film print technology (screen print technology) and the sand blasting. Providing the stripe barrier 207E of a width on the order of 10-100 μm and a height on the order of 100 - 200 μm is very difficult compared with providing the barrier 207E only in one direction. Moreover, the accuracy required when front glass substrate 205 carrying the first electrode 207 and the rear glass substrates 206 carrying the address electrode 209 are attached to each other can be less strict so that the high resolution can be achieved if the stripe barrier 207E is provided only in one direction than the accuracy required when the stripe barrier 207E is provided.

[0029] In addition, when the high resolution is intended, this stripe barrier 207E is a factor making the process for manufacturing the plasma display panel 2 more difficult. Moreover, even if the stripe barrier 207E is not provided, it is necessary to narrow the non-discharge slit if the high resolution is intended. In the plasma display panel 2 characterized by a narrow non-discharge slit, the space charge freely extends to the space in the vertical direction, and an unnecessary effect of the priming is generated for the cells adjoining in the vertical direction, resulting in unnecessary collection of the wall charge 204. As a result, an improper discharge (mis-addressing) is generated. Such a phenomenon is called a vertical connection.

[0030] Next, the generation mechanism of the vertical connection is explained with reference to Figs. 7A - 7C. The address discharge to select the display cell is caused by giving the voltage of less than the minimum discharge-initiating voltage and more than the minimum sustain discharge voltage to the X electrode 207 and the Y electrode 208, and by giving, to the address electrode 209 forming the cell to be selected, the address pulse (voltage V_a) of a level by which the potential difference with respect to the Y electrode 208 exceeds the discharge-initiating voltage between the address electrode 209 and the Y electrode 208.

[0031] The voltage of V_X (50 V) is applied to the X electrode 207 as shown in Figs. 7A - 7C. Moreover, the scan pulse 21 of the selection potential $-V_Y$ (-150 V) is applied to the Y electrode 208. At this time, the address pulse of V_a (50 V) (voltage V_a) is applied to the address electrode 209 of the cell selected for the discharge so that the discharge is begun. Here, when the discharge-initiating voltage between the address electrode 209 and the Y electrode 208 is assumed to be V_fAY , the relation of $V_fAY \leq V_a + V_Y (=200 \text{ V})$ exists. Moreover, when the minimum sustain discharge voltage between the X

electrode 207 and the Y electrode 208 is assumed to be V_{sm} and the discharge-initiating voltage between the X electrode 207 and the Y electrode 208 is assumed to be V_f , the relation of $V_{sm} \leq V_X + V_Y$ (200 V) < V_f exists.

[0032] The discharge begun between the address electrode 209 and the Y electrode 208 (the first step) triggers and activates the discharge between the X electrode 207 and the Y electrode 208 (the second step). When the discharge is settled in the final (the 3rd) stage, the negative wall charge 204 is collected on the X electrode 207 side, the positive wall charge 204 is collected on the Y electrode 208 side, and the negative wall charge 204 is collected on the address electrode 209 side, respectively.

[0033] Next, a description will now be given of an influence on the adjacent lines. Figs. 9 through 12 are referred to for an explanation of the influence on the adjacent cells occurring in the address discharge.

[0034] Referring to Figs. 9 through 12, three cells consecutive in the vertical direction are formed by an X1 electrode 207-1 and a Y1 electrode 208-1, an X2 electrode 207-2 and a Y2 electrode 208-2, and an X3 electrode 207-3 and a Y3 electrode 208-3, respectively.

[0035] Fig. 9 shows that the address discharge is not caused in the cell formed by the electrode 208-1 since display data is not supplied thereto and that the address discharge is caused in the cell of the Y2 electrode 208-2. The voltage applied to the X1 electrode 207-1 adjacent to the Y2 electrode 208-2 is the same as the voltage V_X (50 V) applied to the X electrode 207 of the selected line 202. Negative charges are drawn to the Y2 electrode 208-2 naturally since this voltage has a positive polarity so that the drawn charges are collected as the wall charge 204. When the wall charge 204 collected on the X1 electrode 207-1 is small in volume, it does not present any problem when the non-discharge slit is as wide as 300 μm as shown in Fig. 9.

[0036] However, as the cell pitch becomes small as shown in Fig. 10 so that the non-discharge slit is as narrow as, for instance, 200 μm , the wall charges 204 is collected in a large amount on the X1 electrode 207-1 side. When the minimum sustain discharge voltage between the X1 electrode 207-1 and the Y2 electrode 208-2, that is, the minimum sustain discharge voltage in the non-discharge slit, is 190 V, the discharge between address electrode 209 and the Y2 electrode 208-2 may trigger the discharge between the X1 electrode 207-1 and the Y2 electrode 208-2, forming the wall charge 204.

[0037] Moreover, as shown in Fig. 11, the discharge with a large scale occurs when the voltage (V_a) of the address pulse (voltage V_a) to be applied to the address electrode 209 is raised from 50 V to 70 V so as to ensure that the discharge between the address electrode 209 and the Y electrode 208 which discharge is the first step of the address discharge properly occurs. As a result, a lot of the wall charge 204 is collected on the X1 electrode 207-1 side.

[0038] Moreover, as shown in Fig. 12, the discharge with a large scale occurs when the voltage (V_x) to be applied to the X electrode 207 is raised from 50 V to 70 V so as to ensure that the discharge between the X electrode 207 and the Y electrode 208 which discharge is the second step of the address discharge properly occurs. As a result, a lot of the wall charge 204 is collected on the X1 electrode 207-1 side.

[0039] That is, there is a problem with the conventional technology in that the improper discharge occurs as a result of a large amount of negative charge collected on the X1 electrode 207-1, causing an improper discharge.

[0040] Next, an unfavorable example of the sustain discharge (vertical connection) is explained by referring to Fig. 13.

[0041] Since the cell of the X1 electrode 207-1 is OFF, the address discharge is not caused before the sustain discharge period is started in Fig. 13. The wall charge 204 collected on the X1 electrode 207-1 may reduce the potential of the X electrode and cause the discharge between X1 and Y1 when the sustain pulse of a voltage V_s (180 V) is applied to the Y electrode 208. Moreover, the potential difference between the X1 electrode 207-1 and the address electrode 209 expands due to the wall charge 204. There is a problem in that the discharge between the X1 electrode 207-1 and the Y1 electrode 208-1 is introduced as a result of a process which corresponds to the first step of the address discharge being caused between the address electrode 209 and the X1 electrode 207-1.

[0042] Next, a description will be given, by referring to Fig. 14, of the malfunction occurring when the address discharge is performed in the plasma display panel 2 with the electrode array shown in Fig. 2.

[0043] When the address discharge involving the Y1 electrode 208-1 ends, the address discharge involving the Y2 electrode 208-2 is caused. The discharge between the Y2 electrode 208-2 and the Y1 electrode 208-1 is begun before the target discharge between the Y2 electrode 208-2 and the X2 electrode 207-2 due to a triggering action of the discharge between the address electrode 209 and the Y2 electrode 208-2 initiated by the scan pulse 21 of -150 V applied to the Y2 electrode 208-2. At this time, the address cycle ends without the discharge between the Y2 electrode 208-2 and the X2 electrode 207-2 being started. There was a problem that the sustain discharge is not initiated in the cell comprising the Y1 electrode 208-1 and in the cell comprising the Y2 electrode 208-2.

[0044] The present invention can ensure that propagation of the space charge from the cell selected for the address discharge is small in scale by ensuring that a lower potential occurs in the non-selected X electrode than the potential of the selected X electrode. This arrangement avoids an unfavourable situation in which a discharge is caused in a line *not* selected, or an improper discharge is caused due to collection of the wall

charge.

[0045] EP-A-0657861 discloses a plasma display device and a method of operating the plasma display panel substantially as described above with reference to Figs. 5 and 6.

SUMMARY OF THE INVENTION

[0046] Accordingly, an object of the present invention is to provide a method of operating a plasma display panel capable of performing a stable discharge in a plasma display panel characterized by a small cell pitch and a narrow non-discharge slit.

[0047] Another and more specific object of the present invention is to provide a high-brightness, high-resolution plasma display panel with a high cost performance.

[0048] According to one aspect of the present invention, there is provided a method of operating a plasma display panel provided with first electrode arrays arranged in rows each formed of a pair of first and second electrodes, and second electrode arrays arranged in columns each formed of a third electrode, each of the first and second arrays being sandwiched between substrates, display cells being formed at the crosspoints of the electrodes of the two arrays, an address discharge process for writing information is caused in a selected cell by applying a pulse to the second electrode and the third electrode forming the selected cell, and said information being displayed such that a sustain discharge is caused by applying, in accordance with the information written as a result of the address discharge process, a sustain pulse to the first and second electrodes forming said display cell, characterised in that said address discharge process is controlled such that a potential difference provided by a selection potential for the first electrode and occurring across a second gap that performs the sustain discharge between the pair of first and second electrodes of a row is greater than a potential difference provided by a non-selection potential for the first electrode and occurring across a first gap that does not perform the sustain discharge between the second electrode of a row and the first electrode of another row adjoining the row.

[0049] According to another aspect of the present invention there is provided a plasma display device provided with a plasma display panel, first electrode arrays arranged in rows each formed of a pair of first and second electrodes, and second electrode arrays arranged in columns each formed of a third electrode, each of the first and second arrays being sandwiched between substrates, display cells being formed at the crosspoints of the electrodes of the two arrays, an address discharge process for writing information is caused in a selected cell by applying a pulse to the second electrode and the third electrode forming the selected cell, and said information being displayed such that a sustain discharge is caused by applying, in accordance with the information

written as a result of the address discharge process, a sustain pulse to the first and second electrodes forming said display cell, said plasma display device being characterised by comprising: first electrode operating means for controlling said address discharge process such that a potential difference provided by a selection potential for the first electrode and occurring across a second gap that performs the sustain discharge between the pair of first and second electrodes of a row is greater than a potential difference provided by a non-selection potential for the first electrode and occurring across a first gap that does not perform the sustain discharge between the second electrodes of a row and the first electrode of another row adjoining the row, sustain discharge.

[0050] One embodiment of the present invention provides a plasma display panel provided with a plasma display panel, first electrode arrays arranged in rows each formed of a pair of first and second electrodes, and second electrode arrays arranged in rows each formed of a third electrode, each of the first and second arrays being sandwiched between substrates so as to form a display line, display cells being formed at the cross-points of the electrodes of the two arrays, wherein a first gap between the second electrode selected for the sustain discharge and the first electrode not selected for the sustain discharge is wider than a second gap between the first and second electrodes selected for the sustain discharge, and said first electrode operating means comprises: a first selection driver for operating, in a first half of the address discharge process, the first electrodes for even display lines by supplying the first electrodes with one of the selection potential and the non-selection potential; a second selection driver operating, in a second half of the address discharge process, the first electrodes for odd display lines; and a common driver for supplying a sustain pulse to all the first electrodes in the sustain discharge following the address discharge process.

[0051] Another embodiment of the present invention provides a plasma display device provided with a plasma display panel, first electrode arrays arranged in rows each formed of a pair of first and second electrodes, and second electrode arrays arranged in rows each formed of a third electrode, each of the first and second arrays being sandwiched between substrates so as to form a display line, display cells being formed at the cross-points of the electrodes of the two arrays, wherein a first gap between the second electrode selected for the sustain discharge and the first electrode not selected for the sustain discharge is wider than a second gap between the first and second electrodes selected for the sustain discharge,

an address discharge for writing information is caused in a cell selected by applying a pulse to the second electrode and the third electrode forming the selected cell, and

said information is displayed such that a sustain discharge is caused by applying, in accordance with the

information written as a result of the address discharge process, a sustain pulse to the first and second electrodes forming said display cell, and

said first electrode operating means comprises:

5 a scan driver provided in each of the first electrodes so as to supply the selection potential and the non-selection potential thereto; and
10 a common driver for supplying a sustain pulse to all the first electrodes in the sustain discharge following the address discharge process.

BRIEF DESCRIPTION OF THE DRAWINGS

15 **[0052]** Other objects and further features of the present invention will be apparent from the following detailed description when read in conjunction with the accompanying drawings, in which:

20 Fig. 1 is a top view showing the construction of a triple-electrode surface-discharge AC plasma display panel having the Y-X-Y-X array electrode structure;

25 Fig. 2 is a top view showing the construction of a triple-electrode surface-discharge AC plasma display panel having the Y-X-X-Y array electrode structure wherein connections between the electrodes are improved so as to reduce the capacitance between the electrodes;

30 Fig. 3 is a sectional view, taken along the address electrode, of the plasma display panel shown in Fig. 1 having the Y-X-Y-X array electrode structure and the plasma display panel shown in Fig. 2 having the Y-X-X-Y array electrode structure;

35 Fig. 4 is a sectional view, taken along the sustain electrode, of the plasma display panel shown in Fig. 1 having the Y-X-Y-X array electrode structure and the plasma display panel shown in Fig. 2 having the Y-X-X-Y array electrode structure;

40 Fig. 5 is a block diagram of a plasma display device provided with a peripheral circuit for operating the plasma display panel shown in Fig. 1 having the Y-X-Y-X array electrode structure and the plasma display panel shown in Fig. 2 having the Y-X-X-Y array electrode structure;

45 Fig. 6 is a waveform chart which explains a conventional method of operating the plasma display panel shown in Figs. 1 - 4 using the circuit shown in Fig. 5, in which method the separated address period/sustain discharge period write addressing is executed and the whole-screen self-erase discharge is carried out in the reset period;

50 Figs. 7A - 7C are diagrams which explain the mechanism of the address discharge;

55 Fig. 8 is a time chart showing the sequence of the separated address period/sustain discharge period write addressing;

Fig. 9 is a diagram which explains how adjacent

cells are affected in the address discharge; Fig. 10 is a diagram which explains how adjacent cells are affected in the address discharge; Fig. 11 is a diagram which explains how adjacent cells are affected in the address discharge; Fig. 12 is a diagram which explains how adjacent cells are affected in the address discharge; Fig. 13 is a diagram which explains an unfavorable example (vertical connection) of the sustain discharge; Fig. 14 is a diagram which explains an unfavorable example (vertical connection) of the sustain discharge; Fig. 15 is a waveform chart in the method of a first embodiment for operating the triple-electrode surface-discharge AC plasma display panel having the Y-X-Y-X array electrode structure; Fig. 16 is a diagram which explains the mechanism of the address discharge in the method of operation according to the first embodiment; Fig. 17 is a block diagram of a circuit for operating a plasma display device according to a second embodiment in which the triple-electrode surface-discharge AC plasma display panel having the Y-X-Y-X array electrode structure is used; Fig. 18 is a block diagram showing a circuit for operating the X electrode in the plasma display device of Fig. 17; Fig. 19 is a timing chart showing the operation of the circuit for operating the X electrode in the plasma display device of Fig. 17; Fig. 20 is a waveform chart showing waveforms applied to the electrodes in the plasma display device of Fig. 17 in one sub-field; Fig. 21 is a waveform chart showing waveforms applied in on sub-field to the electrodes in the plasma display device according to a third embodiment in which the triple-electrode surface-discharge AC plasma display panel having the Y-X-Y-X array electrode structure is used; Fig. 22 is a waveform chart showing waveforms applied in one sub-field to the electrodes in the plasma display device according to a fourth embodiment in which the triple-electrode surface-discharge AC plasma display panel having the Y-X-Y-X array electrode structure is used; Fig. 23 is a block diagram showing a main part of a circuit for operating the plasma display device according to a fifth embodiment in which the triple-electrode surface-discharge AC plasma display panel having the Y-X-Y-X array electrode structure is used; Fig. 24 is a diagram showing waveforms applied to the electrodes in the plasma display device of Fig. 23 during one sub-field; Fig. 25 is a diagram which explains the mechanism of the address discharge in the method of operation according to an example not embodying the present

invention; and

Fig. 26 is a waveform chart showing waveforms applied in on sub-field to the electrodes in the plasma display device according to the Fig. 25 example in which the triple-electrode surface-discharge AC plasma display panel having the Y-X-Y-X array electrode structure is used.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

10

[0053] A description will be given in the following, with reference to Figs. 1 through 4, of the AC plasma display panel.

15 [0054] The AC plasma display panel used in the embodiments of the present invention have the same structure as the conventional AC plasma display panel 2 shown in Fig. 1 through 4. However, the plasma display panel of the present invention differs from that of the conventional technology in that the distance of the non-discharge slit is set to 200 µm.

20 [0055] Two types of AC plasma display panels are known: a dual-electrode plasma display panel in which two electrodes are used to perform an address discharge and a sustain discharge; and a triple-electrode plasma display panel in which three electrodes are used to perform the address discharge. A triple-electrode surface-discharge AC plasma display panel is normally used as a color display panel. A triple-electrode surface-

25 discharge AC plasma display panel may be constructed such that a third electrode is formed on the same substrate on which first and second electrodes selected for the sustain discharge are formed. Alternatively, the triple-electrode surface-discharge AC display panel may be constructed such that a third electrode is formed on a separate substrate facing the substrate on which the first and second electrodes are formed.

30 [0056] The plasma display panel in which the three electrodes are formed on the same substrate may be constructed such that the third electrode is provided above the two electrodes for the sustain discharge. Alternatively, the third electrode is formed below the two electrodes for the sustain discharge. According to another classification, a plasma display panel may be a

35 transparent plasma display panel constructed such that visible light emitted and transmitted by the fluorescent body is observed human eyes. A reflection plasma display panel is constructed such that the reflection from the fluorescent body is observed.

40 [0057] A cell in which a discharge takes place is separated from the adjacent cells by ribs or barriers. Ribs or barriers may be provided to surround on all four sides a cell in which a discharge takes place. Alternatively, a rib or a barrier may be provided to cover one of the four sides of the cell so that, on the remaining three sides, the cell is separated from the adjacent cells by optimizing gaps between electrodes.

45 [0058] A description will now be given of a reflection

triple-electrode surface-discharge AC plasma display panel in which the third electrode is formed on a substrate facing the substrate on which the electrodes for the sustain discharge are formed, ribs are formed only in an orthogonal direction (that is, in a direction perpendicular to the direction in which the sustain electrodes lie and parallel to the direction in which the third electrode lies), and each of the sustain electrodes is formed in part by a transparent electrode.

[0059] Fig. 1 shows such a reflection triple-electrode surface-discharge AC plasma display panel 2. Fig. 2 shows another triple-electrode surface-discharge AC plasma display panel 2 which is an elaboration of the panel of Fig. 2 in that the disposition of the electrodes is improved so that the capacitance between electrodes is reduced.

[0060] The triple-electrode surface-discharge AC plasma display panel 2 shown in Fig. 1 in which the first electrode 207 (X electrode) and the second electrode 208 (Y electrode) are alternately arranged will be referred to as a Y-X-Y-X array triple-electrode surface-discharge AC plasma display panel. The triple-electrode surface-discharge AC plasma display panel 2 shown in Fig. 2 in which one of the first electrode 207 (X electrode) and two of the second electrodes 208 (Y electrode) are alternately arranged will be referred to as a Y-X-X-Y array triple-electrode surface-discharge AC plasma display panel.

[0061] Fig. 3 is a sectional view of the triple-electrode surface-discharge AC plasma display panel having the Y-X-Y-X array or the Y-X-X-Y array taken along the direction in which the third electrodes 209 lie. Fig. 4 is a sectional view of the plasma display panel having the Y-X-Y-X array or the Y-X-X-Y array taken along the direction in which the sustain electrodes lie.

[0062] As shown in Figs. 3 and 4, the triple-electrode surface-discharge AC plasma display panel having the Y-X-Y-X array or the Y-X-X-Y array includes a rear glass substrate 206 and a front glass substrate 205. A first electrode 207 (specifically, X electrode) and a second electrode 208 (specifically, Y electrode) are formed in the front glass substrate 205 with a separation of a discharge slit (that is, a gap between the X electrode 207 and the Y electrode 208 set to about 100 µm). A pair formed by the first electrode 207 and the second electrode 208 constitutes a sustain electrode. Each of these electrodes 207, 208 is composed by a transparent electrode 207A and a bus electrode 207B. The transparent electrode 207A lets a reflected beam 207H from a fluorescent body 207 to pass therethrough. The bus electrode 207B is provided to prevent a voltage drop by an electrode resistance. In addition, the electrodes are coated by a dielectric layer 207C and a MgO (magnesium oxide) film 207D is formed on the discharge side as a protective film. Moreover, a third electrode (address electrode) 209 is formed in the second substrate 206 (specifically, the rear glass substrate 206) opposite to front glass substrate 205 so as to be orthogonal to the

first electrode 207. Moreover, a barrier 207E is formed between the address electrodes 209 protected with a diolocitic 207G. A fluorescent body 207F with a red, green, blue luminescence characteristic is formed so as to cover the address electrode 209 between the barriers 207E. The rear glass substrate 206 and the front glass substrate 205 are assembled such that a ridge of the barrier 207E and the MgO film 207D are in close contact with each other. Moreover, when the discharge slit between the first electrode 207 and the second electrode 208 which form the pair is set to 100 µm, the non-discharge slit which is a gap between two adjacent sustain electrodes in the respective display lines is set to 200 µm. The width of the sustain electrode is set to about 250 µm.

[0063] Next, various embodiments of the present invention will be explained based on the drawings. The first embodiment will now be explained. The method of operation in the first embodiment is a method of operating the triple-electrode plane-discharge AC plasma display panel 2 which has the Y-X-Y-X array electrode structure. It is a method of operation by which the potential of the non-selected x electrode 207 occurring on non-discharge slit side is made lower than the potential of the selected X electrode 207 during the address discharge period according to the separated address period/sustain discharge period write addressing.

[0064] The plasma display panel 2 used the method of operation according to the first embodiment has a Y-X-Y-X array electrode structure shown in Fig. 1. The discharge slit is set to 100 µm and the non-discharge slit is set to 200 µm.

[0065] Fig. 15 is a waveform chart of the method of operation according to the first embodiment. The method of the first embodiment is such that the voltage of the selected X electrode 207 is made different from that of the non-selected X electrode 207. Alternatively, the voltage of the selected X electrode 207 (e.g. the Xn electrode) is made different from that of the X electrode 207 (e.g. the Xn-1 electrode) opposite to the selected X electrode 207 across the non-discharge slit (e.g. between the Yn and Xn-1 electrodes).

[0066] Specifically, the voltage VX (50 V in Fig. 15) is applied to the selected X electrode 207 only as long as an address cycle (specifically, 3 ms), which is an addressing time for each display line. A voltage (specifically, 0 V-100 V) lower than the voltage VX (50 V) is applied to the X electrode 207 not selected. Moreover, -150 V is applied to the selected Y electrode 208 and -50 V is applied to the Y electrode 208 not selected. An optimum value of the voltage applied to the X electrode 207 not selected is determined according to the structure of the cell (electrode). The voltage applied to the X electrode 207 not selected is set to an optimum level by which the space charge is not drawn from the adjacent line or set to an optimum level less than the minimum sustain discharge voltage on the non-discharge slit side.

[0067] When the triple-electrode plane-discharge AC

plasma display panel 2 which has the Y-X-Y-X array electrode structure of the above-mentioned is operated with the address cycle of 3 ms shown in Fig. 15, the discharge-initiating voltage V_f between the X electrode 207 and the Y electrode 208 has a level beyond 200 V. As a result, the discharge-initiating voltage V_f of all cells in the plasma display panel 2 reaches a level between 230 V-250 V. Specifically, due to slight production variations the discharge-initiating voltage between the X1 and Y electrodes is $V_{f1}=230$ V and the discharge-initiating voltage between the X_n and Y electrodes is $V_{fn}=250$ V. The minimum sustain discharge voltage between X electrode 207 and Y electrode 208 is specifically 150 V.

[0068] When the minimum sustain discharge voltage V_{sm} is set to 150 V, the sustain discharge voltage V_s is set such that $150 \text{ V} \leq \text{sustain discharge voltage} < 230 \text{ V}$. According to the first embodiment, the sustain discharge voltage V_s is set to 180 V.

[0069] A further description will be given of the method of operating the plasma display panel. Fig. 16 explains the mechanism of the address discharge in the method of operation in the first embodiment.

[0070] As shown in Fig. 16, the potential difference between the X electrode 207 and the Y electrode 208 in the addressing is set within the range of the sustain discharge voltage V_s according to the method of operation in the first embodiment. However, in order ensure that the second step of the address discharge is performed more properly, a sum of the sustain discharge voltage V_s and the voltage applied to the Y electrode 208 is equal to $V_Y=200$ V. As a result, when the voltage $-V_Y$ applied to the Y electrode 208 selected is set to -150 V, the voltage V_X of the X electrode 207 is 50 V. The minimum sustain discharge voltage V_{sm} in the non-discharge slit becomes 190 V for instance. The discharge in the non-discharge slit is generated if an effect of the priming is available when the potential of all the X electrodes 207 are set to 50 V at addressing. In addition, since due to slight production variations the discharge-initiating voltage V_{fAY1} between the address electrode 209 and the Y_1 electrode 208 is 170 V and the discharge-initiating voltage V_{fAYn} between the address electrode 209 and the Y_n electrode 208 is 190 V, a potential difference between the voltage $-V_Y$ applied to the selected Y electrode 208 and the voltage V_a of the address pulse to be applied to the address electrode 209 must exceed 190 V above. Thus, it is preferable to set the voltage V_a of the address pulse applied to the address electrode 209 to 50 V when the voltage $-V_Y$ applied to the selected Y electrode 208 is -150 V.

[0071] As has been described, the method of operating a plasma display according to the first embodiment is such that the potential of the X electrode 207 of the non-selected line adjacent to the selected Y electrode 208 is set to 0 V (50 V, in the conventional method of operation) during the address discharge for writing of display data. It is possible to set the potential difference between the selected Y electrode 208 and the X elec-

trode 207 not selected to 150 V, which is lower than the minimum sustain discharge voltage V_{sm} (=190 V) of the non-discharge slit. As a result, even if the plasma display panel 2 has an inter-electrode gap similar to that of the conventional plasma display panel, no collection of the negative charge occurs on the X electrode 207. It is possible to execute a normal address discharge for this non-selected line when it is due in the next address cycle, for example. Further, even when the sustain pulse is applied to the non-selected line, no erroneous discharge is caused. In addition, since the non-discharge slit can be narrowed, a high-brightness and high-resolution plasma display device can be manufactured.

[0072] Next, the second embodiment is explained based on the drawings. Fig. 17 is a block diagram of an operation circuit for operating a plasma display device 10 according to the second embodiment which uses the triple-electrode plane-discharge AC plasma display panel 10 having the Y-X-Y-X array electrode structure.

[0073] An address pulse for the address discharge is applied to the address electrode 209 using an address driver 28 connected to each of address electrode 209 in the plasma display device 10. The address driver 28 is controlled by a control circuit 281. Moreover, the Y electrode 208 is individually connected to a scan driver 27 (Y scan driver 27). The X electrode 207 is connected together over all display lines 201 of a plasma display panel 2. The X electrode 207 is connected to an X selection driver 23 (a first selection driver). A X-side common driver 22 (sustain pulse applying means) generates the write pulse, and the sustain pulse, etc. and is controlled by a common driver control unit 221. The common driver control unit 221, the scan driver control unit 271, and the control circuit 281 are controlled with a vertical sync signal (VSYNC in Fig. 17) and a horizontal sync signal (Hsync in Fig. 17) input from outside the device to the panel operation control unit 281A, and with a display data signal (DATA in Fig. 17) and a dot clock (CLOCK in Fig. 17) input to a display data control unit 281B. The display data signal DATA input according to the dot clock CLOCK is stored in a frame memory 281B-1. The Y scan driver 27 (second electrode operation means) is connected to the Y-side common driver 22 and the pulse for the address discharge is generated by the scan driver 27. The sustain pulse etc. are generated by the Y-side common driver 22 (sustain pulse applying means) and these pulses are applied to the Y electrode 208 via the Y scan driver 27 (second electrode operation means). The Y-side common driver 22 is controlled by the common driver control unit 221 provided in the panel operation control unit 281A. The Y scan driver 27 and the X selection driver 23 are controlled by the scan driver control unit 271 installed in the panel operation control unit 281A. Those elements that are the same as the elements that are the same as the elements described in the first embodiment are designated by the same reference numerals, and the description thereof is omitted.

An address pulse for the address discharge is applied to the address electrode 209 using an address driver 28 connected to each of address electrode 209 in the plasma display device 10. The address driver 28 is controlled by a control circuit 281. Moreover, the Y electrode 208 is individually connected to a scan driver 27 (Y scan driver 27). The X electrode 207 is connected together over all display lines 201 of a plasma display panel 2. The X electrode 207 is connected to an X selection driver 23 (a first selection driver). A X-side common driver 22 (sustain pulse applying means) generates the write pulse, and the sustain pulse, etc. and is controlled by a common driver control unit 221. The common driver control unit 221, the scan driver control unit 271, and the control circuit 281 are controlled with a vertical sync signal (VSYNC in Fig. 17) and a horizontal sync signal (Hsync in Fig. 17) input from outside the device to the panel operation control unit 281A, and with a display data signal (DATA in Fig. 17) and a dot clock (CLOCK in Fig. 17) input to a display data control unit 281B. The display data signal DATA input according to the dot clock CLOCK is stored in a frame memory 281B-1. The Y scan driver 27 (second electrode operation means) is connected to the Y-side common driver 22 and the pulse for the address discharge is generated by the scan driver 27. The sustain pulse etc. are generated by the Y-side common driver 22 (sustain pulse applying means) and these pulses are applied to the Y electrode 208 via the Y scan driver 27 (second electrode operation means). The Y-side common driver 22 is controlled by the common driver control unit 221 provided in the panel operation control unit 281A. The Y scan driver 27 and the X selection driver 23 are controlled by the scan driver control unit 271 installed in the panel operation control unit 281A. Those elements that are the same as the el-

ements described with reference to Fig. 5 are designated by the same reference numerals and the description thereof is omitted.

[0074] Fig. 18 is a block diagram which shows X selection driver 23 (first electrode operation means) which is the operation circuit of the X electrode 207 in the plasma display device 10 of Fig. 17. The X common driver 30 (sustain pulse applying means) is connected to the X selection driver 23 (sustain pulse applying means) so as to generate the sustain pulse (sustain discharge voltage Vs) etc. applied to all the X electrodes 207. The X selection driver 23 (first electrode operation means) is composed of the circuit to give a voltage to an odd X electrode group and an even X electrode group independently during the address period. As shown in Fig. 18, each of the X selection driver 23 and the X common driver 30 is composed respectively of FETs (field-effect transistors), which are switching elements 25, and of diodes 26. Moreover, the X common driver 30 and the X selection driver 23 are mutually connected through the diodes 26.

[0075] A power supply 29 (power supply voltage Vx) of the X selection driver 23 is the same as the power supply 29 (power supply voltage Va) of the address driver. Fig. 19 is a timing chart which shows the operation of the X selection driver 23 of the X electrode 207 in the plasma display device 10 of Fig. 17.

[0076] During the address period, the selection potential Vx (50 V) is applied to the odd electrode group by AUI and ADI which are FET25. At this time, the even number line is fixed to 0 V and maintained at the non-selection potential by turning on AC2. On the other hand, when the even number line is addressed, 50 V is given to the even X electrode group by AU2 and AD2. At this time, the odd electrode group is fixed to 0 V by AC1.

[0077] Fig. 20 is a waveform chart showing the waveform applied to the electrode in the plasma display device 10 of Fig. 17 during one sub-field period. The method of operation according to the second embodiment is the separated address period/sustain discharge period write addressing. As with the conventional technology, the method of operation according to the second embodiment causes all cells on the screen to be uniform by applying thereto the voltage pulse for the whole-screen write discharge and the whole-screen self-erase during the reset period.

[0078] In the method of the operation according to the second embodiment, the address discharge is executed from the first line one by one as shown in Fig. 20 when the reset period ends and the address period is started. Initially, all the address electrodes 209 are set to 0 V, all the X electrodes 207 to 0 V, and all the Y electrodes 208 to -50 V (-Vsc). When the address cycle of the first line is started, 50 V is given to the X electrode 207 and the voltage of -150 V (the voltage -VY applied to the selected Y electrode 208) is applied to the Y electrode 208, respectively. The address pulse (voltage Va) of a level

5 50 V is applied to the address electrode 209 corresponding to the cell selected for display (luminescence and sustain discharge). On this selected cell, the discharge occurring between the address electrode 209 and the Y electrode 208 triggers the discharge between the X electrode 207 and the Y electrode 208. As a result, the negative wall charge 204 is collected on the MgO film 207D on the X electrode 207, and the positive wall charge 204 is collected on the MgO film 207D on the Y electrode 208, whereupon the discharge is extinguished. The negative wall charge 204 is formed in the fluorescent 207F on the address electrode 209.

[0079] It is now assumed that the display of the first line is OFF and the address discharge is performed in the second display line. The potential of the X electrode 207 (X1 electrode 207-1) not selected is maintained at 0 V in the address cycle for the second line. Therefore, the negative wall charge 204 is not formed (or formed only in a small quantity) on the X1 electrode 207-1 opposite to the Y2 electrode 208-2 across the non-discharge slit. Therefore, the display cell formed by the X1 electrode 207-1 is prevented from being lit when the sustain discharge begins.

[0080] Subsequent address cycles are carried out until addressing for all the display lines is completed.

[0081] When the addressing is completed, the sustain discharge period is initiated. The sustain discharge is performed only in those cells in which the wall discharge 204 is collected as a result of the address discharge. 15 The sustain discharge is repeated a predetermined number of times in order for a sequence for one sub-field to be completed.

[0082] As described before, the voltage of the selected X electrode 207 is made different from that of the non-selected X electrode 207. No wall charge 204 is collected in the line adjacent to the target line. The potential difference (150 V) between the potential (-150 V) of the selected Y electrode and the potential (0V) of the adjacent non-selected X electrode 207 is smaller than the minimum sustain discharge voltage Vsm in the non-discharge slit. Therefore, no discharge occurs even when the space charge is propagated to the non-discharge slit. The entire address period is divided into a first half and a second half, so that the even lines and the odd lines are independently addressed. This arrangement has an effect of reducing the power consumed when the X electrode 207 is selected, preventing a vertical connection between the adjacent cells, and enabling a proper display with no mis-addressing.

[0083] Since the pulse for selecting the X electrode 207 has the same polarity and voltage level as the address pulse (voltage Va) applied to the address electrode 209, the power consumption can be reduced and the necessary circuitry can be prepared easily. Since the scan driver is provided for the X electrode 207, a more efficient operation is enabled.

[0084] A description will now be given of a third embodiment.

[0085] Fig. 21 is a waveform chart of the voltage applied during one sub-field to the electrodes in the plasma display device 10 according to the third embodiment in which the triple-electrode surface-discharge AC plasma display panel 2 having the Y-X-Y-X array electrode structure is used. Those elements that are the same as the elements of the first and second embodiments described above are designated by the same reference numerals and the description thereof is omitted.

[0086] In the plasma display device 10 according to the second embodiment, a sequential addressing in the odd lines may cause the voltage V_X to be applied to the X electrode 207 not selected for display even though such an electrode belongs to the odd display lines, resulting in a large power consumption due to the charging between the associated electrodes. The same is true of the even display lines.

[0087] In the plasma display device 10 according to the third embodiment operated using the circuit shown in Figs. 5 and 17, the odd display lines are first subject to sequential addressing, and then the even display lines are subject to sequential addressing. That is, the address period for the odd display lines are made independent from the address period for the even display lines. As a result, unnecessary switching actions are prevented from occurring and the power consumption can be reduced.

[0088] As described before, the voltage of the selected X electrode 207 is made different from that of the non-selected X electrode 207. No wall charge 204 is collected in the line adjacent to the target line. The potential difference (150 V) between the potential (-150 V) of the selected Y electrode and the potential (OV) of the adjacent non-selected X electrode 207 is smaller than the minimum sustain discharge voltage V_{sm} in the non-discharge slit. Therefore, no discharge occurs even when the space charge is propagated to the non-discharge slit. The entire address period is divided into a first half and a second half, so that the even lines and the odd lines are independently addressed. This arrangement has an effect of reducing the power consumed when the X electrode 207 is selected, preventing a vertical connection between the adjacent cells, and enabling a proper display with no mis-addressing.

[0089] Since the pulse for selecting the X electrode 207 has the same polarity and voltage level as the address pulse (Voltage V_a) applied to the address electrode 209, the power consumption can be reduced and the necessary circuitry can be prepared easily. Since the scan driver is provided for the X electrode 207, a more efficient operation is enabled.

[0090] A description will now be given of a fourth embodiment.

[0091] Fig. 22 is a waveform chart of the voltage applied during one sub-field to the electrodes in the plasma display device 10 according to the fourth embodiment in which the triple-electrode surface-discharge AC plasma display panel 2 having the Y-X-Y-X array electrode

structure is used. Those elements that are the same as the elements of the first through third embodiments described above are designated by the same reference numerals and the description thereof is omitted.

[0092] In the plasma display device 10 according to the fourth embodiment, the non-selection potential applied to the X electrode 207, the Y electrode 208 and the address electrode 209 during the address period is controlled to 0 V. The address pulse applied to the address electrode 209 is controlled to 100 V, the X selection potential is controlled to 100 V, and the scan pulse for the Y electrode 208 is controlled to -100 V. Since the potential for the non-selected electrodes is set to 0 V and the selection potentials are controlled to the same absolute level of amplitude with respect to the reference level of 0 V, no unfavorable effect is caused in the cells which are not activated.

[0093] As described before, the voltage of the selected X electrode 207 is made different from that of the non-selected X electrode 207. No wall charge 204 is collected in the line adjacent to the target line. The potential difference (100 V) between the potential (-100 V) of the selected Y electrode and the potential (OV) of the adjacent non-selected X electrode 207 is smaller than the minimum sustain discharge voltage V_{sm} in the non-discharge slit. Therefore, no discharge occurs even when the space charge is propagated to the non-discharge slit. The entire address period is divided into a first half and a second half, so that the even lines and the odd lines are independently addressed. This arrangement has an effect of reducing the power consumed when the X electrode 207 is selected, preventing a vertical connection between the adjacent cells, and enabling a proper display with no mis-addressing.

[0094] Since the pulse for selecting the X electrode 207 has the same polarity and voltage level as the address pulse (voltage V_a) applied to the address electrode 209, the power consumption can be reduced and the necessary circuitry can be prepared easily. Since the scan driver is provided for the X electrode 207, a more efficient operation is enabled.

[0095] A description will now be given of a fifth embodiment.

[0096] Fig. 23 is a block diagram of a main part of a driving circuit in the plasma display device 10 according to the fifth embodiment in which the triple-electrode surface discharge AC plasma display panel 2 having the Y-X-Y-X array electrode structure is used. Those elements that are the same as the elements of the first through fourth embodiments described above are designated by the same reference numerals and the description thereof is omitted.

[0097] The plasma display device 10 of Fig. 23 differs from the plasma display device of Figs. 5 and 17 in that the X electrode 207 is operated by the X scan driver 31 (first electrode operating means). The X electrodes 207 in respective display lines are independently connected to the X scan driver 31 and also to the X common driver

30. The Y electrodes 208 are connected to the Y scan driver (second electrode operating means) and the Y common driver 22 (second electrode operating means). [0098] In synchronism with the selection of the display line using the Y scan driver 27, the selection potential VX may be applied by the X scan driver 31 to the X electrode 207.

[0099] In the fourth embodiment, the selection potential has the same voltage of 50 V as the address pulse applied to the address electrode 209.

[0100] Fig. 24 is a waveform chart of a voltage applied during one sub-field to the electrodes in the plasma display device 10 of Fig. 23.

[0101] The addressing in the first display lines is performed such that a scan pulse of -VY (-150 V) is applied to the Y electrode 208-1 selected for display. At the same as this, an X scan pulse of VX (50 V) is applied to the X1 electrode 207-1. The X electrode 207 of the non-selected display line is maintained at 0 V.

[0102] As described before, the voltage of the selected X electrode 207 is made different from that of the non-selected X electrode 207. No wall charge 204 is collected in the line adjacent to the target line. The potential difference (150 V) between the potential (-150 V) of the selected Y electrode and the potential of the adjacent non-selected X electrode 207 is smaller than the minimum sustain discharge voltage Vsm in the non-discharge slit. Therefore, no discharge occurs even when the space charge is propagated to the non-discharge slit. Since the pulse for selecting the X electrode 207 has the same polarity and voltage level as the address pulse (voltage Va) applied to the address electrode 209, the power consumption can be reduced and the necessary circuitry can be prepared easily. Since the scan driver is provided for the X electrode 207, a more efficient operation is enabled.

[0103] A description will now be given of an example not embodying the present invention.

[0104] Fig. 25 shows a mechanism of address discharge according to the method of operation of this example. Fig. 26 is a waveform chart of the voltage applied during one sub-field to the electrodes in the plasma display device 10 according to the Fig. 25 example in which the triple-electrode surface-discharge AC plasma display panel 2 having the Y-X-X-Y array electrode structure is used. Those elements that are the same as the elements of the first through fifth embodiments described above are designated by the same reference numerals and the description thereof is omitted,

[0105] According to the method of operation of this example, the odd display lines are sequentially selected for address discharge in the first half of the address period. When the address discharge is completed for all the odd display lines, the sustain pulse (sustain discharge voltage Vs) is applied to the Y electrode 208. As a result of the sustain discharge in the odd display lines, the wall charge 204 of an inverse polarity is formed in the cells forming the odd display lines (see Fig. 25).

Thereafter, in the second half of the address period, the address discharge is performed in the even display lines. Since the wall charge 204 on the Y electrode 208 forming the odd display lines is negative, no discharge occurs between the adjacent Y electrodes 208 one forming the odd display line and the other forming the even display line.

[0106] The present invention is not limited to the above described embodiments, and variations and modifications may be made without departing from the scope of the present invention.

Claims

1. A method of operating a plasma display panel provided with first electrode arrays arranged in rows each formed of a pair of first and second electrodes (207, 208), and second electrode arrays arranged in columns each formed of a third electrode, each of the first and second arrays being sandwiched between substrates (205), display cells being formed at the crosspoints of the electrodes of the two arrays, an address discharge process for writing information is caused in a selected cell by applying a pulse to the second electrode (208) and the third electrode (209) forming the selected cell, and said information being displayed such that a sustain discharge is caused by applying, in accordance with the information written as a result of the address discharge process, a sustain pulse to the first and second electrodes (207, 208) forming said display cell, **characterised in that**
said address discharge process is controlled such that a potential difference provided by a selection potential for the first electrode (207) and occurring across a second gap that performs the sustain discharge between the pair of first and second electrodes (207, 208) of a row is greater than a potential difference provided by a non-selection potential for the first electrode (207) and occurring across a first gap that does not perform the sustain discharge between the second electrode (208) of a row and the first electrode (207) of another row adjoining the row.
2. The method of operating a plasma display panel as claimed in claim 1, wherein said non-selection potential is controlled to be lower than a minimum sustain voltage for said first gap (210), when the address discharge process is caused.
3. The method of operating a plasma display panel as claimed in claim 1, wherein,
in a first half of an address discharge process, one of even display lines (201) and odd display lines (201) are selected for display by selecting corresponding ones of the second electrodes (208), and,

in a second half of the address discharge process, the other of the even display lines (201) and the odd display lines (201) are selected for display, and

in the first half of the address discharge process, the first electrodes (207) for the one of the even display lines (201) and the odd display lines (201) are set to the selection potential and the first electrodes (207) for the other of the even display lines (201) and the odd display lines (201) are set to the non-selection potential, and in the second half of the address discharge process, the first electrodes (207) for the other of the even display lines (201) and the odd display lines (201) are set to the selection potential and the first electrodes (207) for the one of the even display lines (201) and the odd display lines (201) are set to the non-selection potential.

4. The method of operating a plasma display panel as claimed in claim 1, wherein a pulse for the selection potential supplied to the first electrode (207) is positive with respect to a ground potential, a pulse supplied to the second electrode (208) for the selected display line is negative with respect to the ground potential, and a pulse supplied to the third electrode (209) for the selected display cell is positive with respect to the ground potential.

5. The method of operating a plasma display panel as claimed in claim 4, wherein a voltage for the selection potential supplied to the first electrode (207) is equal to a voltage supplied to the third electrode (209) for the selected display cell.

6. The method of operating a plasma display panel as claimed in claim 4, wherein a voltage for the non-selection potential supplied to the first electrode (207) is equal to a voltage supplied to the third electrode (209) for the display cell not selected.

7. The method of operating a plasma display panel as claimed in claim 6, wherein the voltage for the non-selection potential supplied to the first electrode (207) and the voltage supplied to the third electrode (209) for the display cell not selected are set to the ground potential.

8. The method of operating a plasma display panel as claimed in claim 4, wherein the selection potential difference having approximately half the magnitude of a potential difference between the first electrode (207) and the second electrode (208) applied in the address discharge process is applied to the first electrode (207) and a scan pulse (21) having approximately half the magnitude of said selection potential difference is applied to the second electrode (208) forming the selected cell.

5 9. The method of operating a plasma display panel as claimed in claim 4, wherein voltages for non-selection potential supplied to the first electrode (207), the second electrode (208) and the third electrode (209) in the address discharge process are set to the ground potential.

10 10. The method of operating a plasma display panel as claimed in claim 1, wherein said selection potential is supplied to the first electrode (207) in synchronism with an application of the scan pulse (21) to the second electrode (208), when the display lines (201) are sequentially selected one by one in the address discharge process.

15 11. A plasma display device (10) provided with a plasma display panel, first electrode arrays arranged in rows each formed of a pair of first and second electrodes (207, 208), and second electrode arrays arranged in columns each formed of a third electrode (209),

each of the first and second arrays being sandwiched between substrates (205), display cells being formed at the crosspoints of the electrodes of the two arrays, an address discharge process for writing information is caused in a selected cell by applying a pulse to the second electrode (208) and the third electrode (209) forming the selected cell, and said information being displayed such that a sustain discharge is caused by applying, in accordance with the information written as a result of the address discharge process, a sustain pulse to the first and second electrodes (207, 208) forming said display cell,

30 said plasma display device (10) being characterised by comprising:

first electrode operating means for controlling said address discharge process such that a potential difference provided by a selection potential for the first electrode (207) and occurring across a second gap that performs the sustain discharge between the pair of first and second electrodes (207, 208) of a row is greater than a potential difference provided by a non-selection potential for the first electrode (207) and occurring across a first gap that does not perform the sustain discharge between the second electrode (208) of a row and the first electrode (207) of another row adjoining the row.

40 45 50 55 55 12. The plasma display device as claimed in claim 11, wherein:

said first gap is wider than said second gap, and said first electrode operating means comprises:

a first selection driver (23) for operating, in

- a first half of the address discharge process, the first electrodes (207) for one of even display lines (201) and odd display lines (201) by supplying the first electrodes (207) with one of the selection potential and the non-selection potential, in a second half of the address discharge process, the first electrodes (207) for the one of the even display lines (201) and the odd display lines (201) by supplying the first electrodes (207) with other of the selection potential and the non-selection potential; a second selection driver (24) for operating, in a first half of the address discharge process, the first electrodes (207) for the other of the even display lines (201) and the odd display lines (201) by supplying the first electrodes (207) with the other of the selection potential and the non-selection potential, in a second half of the address discharge process, the first electrodes (207) for other of even display lines (201) and odd display lines (201) by supplying the first electrodes (207) with the one of the selection potential and the non-selection potential; and a common driver (22) for supplying a sustain pulse to the first electrodes (207) commonly, in the sustain discharge following the address discharge process.
13. The plasma display device (10) as claimed in claim 11, wherein each of said first selection driver (23) and the second selection driver (24) comprises switching elements (25).
14. The plasma display device (10) as claimed in claim 13, wherein each of said first selection driver (23) and the second selection driver (24) comprises:
- a first selection element (25) for supplying said selection potential to the first electrode (207);
 - a second switching element (25) for switching a potential of the first electrode (207) to said selection potential;
 - a third switching element (25) for fixing a potential of the first electrode (207) to said non-selection potential.
15. The plasma display device (10) as claimed in claim 11, wherein said common driver (22) for supplying said sustain pulse (22a) to the first electrode (207) has a current draw-in path and a current supply path which are separated from each other, each of said current draw-in path and current supply path being connected to said first selection driver (23), the second selection driver (24) and the first electrodes (207).
- 5 16. The plasma display device (10) as claimed in claim 15, wherein first and second diodes (26) are connected to said current supply path of said common driver (22) in a forward arrangement with respect to the first electrodes (207), third and fourth diodes (26) are connected to said current drawing path in a reverse arrangement with respect to the first electrodes (207), a first selection circuit is connected to a joint between said first and third diodes (26), and the first electrodes (207), and a second selection circuit is connected to a joint between said second and fourth diodes (26), and the first electrodes (207).
- 10 17. The plasma display device (10) as claimed in claim 11, wherein a voltage lower than a voltage supplied to the first electrode (207) adjacent to the second gap (211) is supplied, using the selection driver, to the first electrode (207) adjacent to the first gap (210) and adjacent to the second electrode (208) and selected for the address discharge process.
- 20 18. The plasma display device (10) as claimed in claim 11, wherein, in a first half of the address discharge process, said selection potential is supplied to one group of first electrodes (207) for one of even display lines (201) and odd display lines (201), using one of the first selection driver and the second selection driver, and said non-selection potential is supplied to the other group of first electrodes (207) for the other of the even display lines (201) and the odd display lines (201),
- 25 the address discharge process is sequentially executed in the one group of first electrodes (207), whereupon the non-selection potential is supplied to the one group of first electrodes (207), and the selection potential is supplied to the other group of first electrodes (207) so that the address discharge process is executed in the other group of first electrodes (207), and
- 30 40 said sustain pulse (22a) is supplied from said common driver to all the first electrodes (207) in the plasma display device (10) so that the sustain discharge is caused for illuminated display on the plasma display device (10).
- 35 45 19. The plasma display device (10) as claimed in claim 11, wherein a power source (29) for the first selection driver (23) and the second selection driver (24) is shared by an address driver (28) for operating said third electrode (209).
- 40 50 20. The plasma display device (10) as claimed in claim 11, wherein said first gap (210), between the first and second electrodes (207, 208), not selected for the sustain discharge is twice as large as the second gap (211), between the first and second electrodes (207, 208), selected for the sustain dis-

charge.

21. A plasma display device (10) as claimed in claim 11, said first electrode operating means comprising:

a scan driver provided in each of the first electrodes (207) so as to supply the selection potential and the non-selection potential thereto; and
10 a common driver for supplying a sustain pulse (22a) to the first electrodes (207) commonly, in the sustain discharge following the address discharge process.

22. The plasma display device (10) as claimed in claim 21, wherein a power source (29) for the scan driver (27) for operating the first electrode (207) also supplies a power to an address driver (28) for operating the third electrode (209).

23. The plasma display device (10) as claimed in claim 21, wherein the first gap (210), between the first and second electrodes (207, 208), not selected for the sustain discharge does not exceed twice the width of the second gap (211), between the first and second electrodes (207, 208), selected for the sustain discharge.

Patentansprüche

1. Verfahren zum Betreiben eines Plasmaanzeigefelds, das mit ersten Elektroden-Arrays, welche in Reihen angeordnet sind, die jeweils aus einem Paar von ersten und zweiten Elektroden (207, 208) gebildet sind, und zweiten Elektroden-Arrays versehen ist, welche in Spalten angeordnet sind, die jeweils aus einer dritten Elektrode gebildet sind,
35 wobei jedes der ersten und zweiten Arrays sandwichartig zwischen Substraten (205) angeordnet ist, Anzeigezellen an den Kreuzungspunkten der Elektroden der beiden Arrays gebildet sind, ein Adressenentladungsprozess zum Schreiben von Informationen in einer ausgewählten Zelle veranlasst wird, indem ein Impuls an die zweite Elektrode (208) und die dritte Elektrode (209) angelegt wird, die die ausgewählte Zelle bilden, und die Informationen so angezeigt werden, dass eine Dauerentladung veranlasst wird, indem, in Übereinstimmung mit den als Ergebnis des Adressenentladungsprozesses geschriebenen Informationen, ein Dauerimpuls an die ersten und zweiten Elektroden (207, 208) angelegt wird, die die Anzeigezelle bilden, **durch gekennzeichnet, dass**
40 der Adressenentladungsprozess so gesteuert wird, dass eine Potentialdifferenz, die durch ein Auswahlpotential für die erste Elektrode (207) vor-

- gesehen wird und quer über einen zweiten Spalt auftritt, der die Dauerentladung vornimmt, zwischen dem Paar von ersten und zweiten Elektroden (207, 208) einer Reihe, größer ist als eine Potentiadifferenz, die von einem Nicht-Auswahlpotential für die erste Elektrode (207) vorgesehen wird und quer über einen ersten Spalt auftritt, der die Dauerentladung nicht vornimmt, zwischen der zweiten Elektrode (208) einer Reihe und der ersten Elektrode (207) einer anderen an die Reihe angrenzenden Reihe.
5
10
15
20
25
30
35
40
45
50
55
2. Verfahren zum Betreiben eines Plasmaanzeigefelds nach Anspruch 1, bei welchem das Nicht-Auswahlpotential gesteuert wird, um niedriger zu sein als eine Mindestdauerspannung für den ersten Spalt (210), wenn der Adressenentladungsprozess veranlasst wird.
3. Verfahren zum Betreiben eines Plasmaanzeigefelds nach Anspruch 1, bei welchem
in einer ersten Hälfte eines Adressenentladungsprozesses, eine von geraden Anzeigezeilen (201) und ungeraden Anzeigezeilen (201) für eine Anzeige ausgewählt werden, indem entsprechende der zweiten Elektroden (208) ausgewählt werden, und, in einer zweiten Hälfte des Adressenentladungsprozesses, die andere der geraden Anzeigezeilen (201) und der ungeraden Anzeigezeilen (201) für eine Anzeige ausgewählt werden, und
in der ersten Hälfte des Adressenentladungsprozesses, die ersten Elektroden (207) für die eine der geraden Anzeigezeilen (201) und der ungeraden Anzeigezeilen (201) auf das Auswahlpotential gesetzt werden, und die ersten Elektroden (207) für die andere der geraden Anzeigezeilen (201) und der ungeraden Anzeigezeilen (201) auf das Nicht-Auswahlpotential gesetzt werden, und, in der zweiten Hälfte des Adressenentladungsprozesses, die ersten Elektroden (207) für die andere der geraden Anzeigezeilen (201) und der ungeraden Anzeigezeilen (201) auf das Auswahlpotential gesetzt werden, und die ersten Elektroden (207) für die eine der geraden Anzeigezeilen (201) und der ungeraden Anzeigezeilen (201) auf das Nicht-Auswahlpotential gesetzt werden.
4. Verfahren zum Betreiben eines Plasmaanzeigefelds nach Anspruch 1, bei welchem ein Impuls für das Auswahlpotential, der der ersten Elektrode (207) zugeführt wird, positiv ist in Bezug auf ein Erdpotential, ein der zweiten Elektrode (208) zugeführter Impuls für die ausgewählte Anzeigezelle negativ ist in Bezug auf das Erdpotential, und ein der dritten Elektrode (209) zugeführter Impuls für die ausgewählte Anzeigezelle positiv ist in Bezug auf das Erdpotential.

5. Verfahren zum Betreiben eines Plasmaanzeigefelds nach Anspruch 4, bei welchem eine Spannung für das Auswahlpotential, die der ersten Elektrode (207) zugeführt wird, gleich ist einer der dritten Elektrode (209) zugeführten Spannung für die ausgewählte Anzeigezelle. 5
6. Verfahren zum Betreiben eines Plasmaanzeigefelds nach Anspruch 4, bei welchem eine Spannung für das Nicht-Auswahlpotential, die der ersten Elektrode (207) zugeführt wird, gleich ist einer der dritten Elektrode (209) zugeführten Spannung für die nicht-ausgewählte Anzeigezelle. 10
7. Verfahren zum Betreiben eines Plasmaanzeigefelds nach Anspruch 6, bei welchem die Spannung für das Nicht-Auswahlpotential, die der ersten Elektrode (207) zugeführt wird, und die der dritten Elektrode (209) zugeführte Spannung für die nicht-ausgewählte Anzeigezelle auf das Erdpotential gesetzt werden. 15
8. Verfahren zum Betreiben eines Plasmaanzeigefelds nach Anspruch 4, bei welchem die Auswahlpotentialdifferenz mit ungefähr der halben Größe einer Potentialdifferenz zwischen der ersten Elektrode (207) und der zweiten Elektrode (208), die im Adressenentladungsprozess angelegt wird, an die erste Elektrode (207) angelegt wird, und ein Scan-Impuls (21) mit ungefähr der halben Größe der Auswahlpotentialdifferenz an die zweite Elektrode (208) angelegt wird, die die ausgewählte Zelle bildet. 20
9. Verfahren zum Betreiben eines Plasmaanzeigefelds nach Anspruch 4, bei welchem Spannungen für das Nicht-Auswahlpotential, die der ersten Elektrode (207), der zweiten Elektrode (208) und der dritten Elektrode (209) im Adressenentladungsprozess zugeführt werden, auf das Erdpotential gesetzt werden. 25
10. Verfahren zum Betreiben eines Plasmaanzeigefelds nach Anspruch 1, bei welchem das Auswahlpotential der ersten Elektrode (207) synchron mit einem Anlegen des Scan-Impulses (21) an die zweite Elektrode (208) zugeführt wird, wenn die Anzeigezeilen (201) sequentiell eine nach der anderen im Adressenentladungsprozess ausgewählt werden. 30
11. Plasmaanzeigeanordnung (10), welche mit einem Plasmaanzeigefeld, ersten Elektroden-Arrays, welche in Reihen angeordnet sind, die jeweils aus einem Paar von ersten und zweiten Elektroden (207, 208) gebildet sind, und zweiten Elektroden-Arrays versehen ist, welche in Spalten angeordnet sind, die jeweils aus einer dritten Elektrode (209) gebildet sind, wobei jedes der ersten und zweiten Arrays sandwichartig zwischen Substraten (205) angeordnet ist, Anzeigezellen an den Kreuzungspunkten der Elektroden der beiden Arrays gebildet sind, ein Adressenentladungsprozess zum Schreiben von Informationen in einer ausgewählten Zelle veranlasst wird, indem ein Impuls an die zweite Elektrode (208) und die dritte Elektrode (205) angelegt wird, die die ausgewählte Zelle bilden, und die Informationen so angezeigt werden, dass eine Dauerentladung veranlasst wird, indem, in Übereinstimmung mit den als Ergebnis des Adressenentladungsprozesses geschriebenen Informationen, ein Dauerimpuls an die ersten und zweiten Elektroden (207, 208) angelegt wird, die die Anzeigezelle bilden, welche Plasmaanzeigeanordnung (10) dadurch gekennzeichnet ist, dass sie umfasst: 35
- sind, eine erste Elektrodenbetriebseinrichtung zum Steuern des Adressenentladungsprozesses, so dass eine Potentialdifferenz, die durch ein Auswahlpotential für die erste Elektrode (207) vorgesehen wird und quer über einen zweiten Spalt auftritt, der die Dauerentladung vornimmt, zwischen dem Paar von ersten und zweiten Elektroden (207, 208) einer Reihe, größer ist als eine Potentialdifferenz, die von einem Nicht-Auswahlpotential für die erste Elektrode (207) vorgesehen wird und quer über einen ersten Spalt auftritt, der die Dauerentladung nicht vornimmt, zwischen der zweiten Elektrode (208) einer Reihe und der ersten Elektrode (207) einer anderen an die Reihe angrenzenden Reihe. 40
12. Plasmaanzeigeanordnung nach Anspruch 11, bei welcher: der erste Spalt breiter ist als der zweite Spalt, und die erste Elektrodenbetriebseinrichtung umfasst: 45
- einen ersten Auswahltreiber (23) zum Betreiben, in einer ersten Hälfte des Adressenentladungsprozesses, der ersten Elektroden (207) für eine von geraden Anzeigezeilen (201) und ungeraden Anzeigezeilen (201), indem den ersten Elektroden (207) eines von dem Auswahlpotential und dem Nicht-Auswahlpotential zugeführt wird, und, in einer zweiten Hälfte des Adressenentladungsprozesses, der ersten Elektroden (207) für die eine der geraden Anzeigezeilen (201) und der ungeraden Anzeigezeilen (201), indem den ersten Elektroden (207) das andere von dem Aus- 50
- 55

- wahlpotential und dem Nicht-Auswahlpotential zugeführt wird; einen zweiten Auswahltreiber (24) zum Betreiben, in einer ersten Hälfte des Adressenentladungsprozesses, der ersten Elektroden (207) für die andere der geraden Anzeigezellen (201) und der ungeraden Anzeigezellen (201), indem den ersten Elektroden (207) das andere von dem Auswahlpotential und dem Nicht-Auswahlpotential zugeführt wird, und, in einer zweiten Hälfte des Adressenentladungsprozesses, der ersten Elektroden (207) für die andere von geraden Anzeigezellen (201) und ungeraden Anzeigezellen (201), indem den ersten Elektroden (207) das eine von dem Auswahlpotential und dem Nicht-Auswahlpotential zugeführt wird; und einen gemeinsamen Treiber (22) zum Zuführen eines Dauerimpulses zu den ersten Elektroden (207) gemeinsam, bei der Dauерentladung, die dem Adressenentladungsprozess folgt.
13. Plasmaanzeigeanordnung (10) nach Anspruch 11, bei welcher jeder von dem ersten Auswahltreiber (23) und dem zweiten Auswahltreiber (24) Schaltelemente (25) umfasst.
14. Plasmaanzeigeanordnung (10) nach Anspruch 13, bei welcher jeder von dem ersten Auswahltreiber (23) und dem zweiten Auswahltreiber (24) umfasst:
- ein erstes Schaltelement (25) zum Zuführen des Auswahlpotentials zur ersten Elektrode (207);
 - ein zweites Schaltelement (25) zum Schalten eines Potentials der ersten Elektrode (207) auf das Auswahlpotential;
 - ein drittes Schaltelement (25) zum Fixieren eines Potentials der ersten Elektrode (207) auf dem Nicht-Auswahlpotential.
15. Plasmaanzeigeanordnung (10) nach Anspruch 11, bei welcher der gemeinsame Treiber (22) zum Zuführen des Dauerimpulses (22a) zur ersten Elektrode (207) einen Stromeinzugsweg und einen Stromzuführweg aufweist, die voneinander getrennt sind, wobei jeder von dem Stromeinzugsweg und dem Stromzuführweg mit dem ersten Auswahltreiber (23), dem zweiten Auswahltreiber (24) und den ersten Elektroden (207) verbunden ist.
16. Plasmaanzeigeanordnung (10) nach Anspruch 15, bei welcher erste und zweite Dioden (26) mit dem Stromzuführweg des gemeinsamen Treibers (22) in einer Vorwärts-Anordnung in Bezug auf die ersten Elektroden (207) verbunden sind, dritte und vierte Dioden (26) mit dem Stromzugweg in einer umgekehrten Anordnung in Bezug auf die ersten Elektroden (207) verbunden sind, eine erste Auswahlschaltung mit einer Verbindung zwischen den ersten und dritten Dioden (26), und den ersten Elektroden (207) verbunden ist, und eine zweite Auswahlschaltung mit einer Verbindung zwischen den zweiten und vierten Dioden (26), und den ersten Elektroden (207) verbunden ist.
17. Plasmaanzeigeanordnung (10) nach Anspruch 11, bei welcher eine Spannung, die niedriger ist als eine der ersten Elektrode (207) benachbart dem zweiten Spalt (211) zugeführte Spannung, unter Verwendung des Auswahltreibers, der ersten Elektrode (207) benachbart dem ersten Spalt (210) und benachbart der zweiten Elektrode (208) zugeführt wird und für den Adressenentladungsprozess ausgewählt wird.
18. Plasmaanzeigeanordnung (10) nach Anspruch 11, bei welcher, in einer ersten Hälfte des Adressenentladungsprozesses, das Auswahlpotential einer Gruppe von ersten Elektroden (207) für eine von geraden Anzeigezellen (201) und ungeraden Anzeigezellen (201), unter Verwendung eines von dem ersten Auswahltreiber und dem zweiten Auswahltreiber, zugeführt wird, und das Nicht-Auswahlpotential der anderen Gruppe der ersten Elektroden (207) für die andere der geraden Anzeigezellen (201) und der ungeraden Anzeigezellen (201) zugeführt wird,
- der Adressenentladungsprozess sequentiell in der einen Gruppe von ersten Elektroden (207) ausgeführt wird, worauf das Nicht-Auswahlpotential der einen Gruppe von ersten Elektroden (207) zugeführt wird, und das Auswahlpotential der anderen Gruppe von ersten Elektroden (207) so zugeführt wird, dass der Adressenentladungsprozess in der anderen Gruppe von ersten Elektroden (207) ausgeführt wird, und
 - der Dauerimpuls (22a) vom gemeinsamen Treiber allen ersten Elektroden (207) in der Plasmaanzeigeanordnung (10) so zugeführt wird, dass die Dauерentladung für eine beleuchtete Anzeige auf der Plasmaanzeigeanordnung (10) veranlasst wird.
19. Plasmaanzeigeanordnung (10) nach Anspruch 11, bei welcher eine Energiequelle (29) für den ersten Auswahltreiber (23) und den zweiten Auswahltreiber (24) von einem Adressentreiber (28) zum Betreiben der dritten Elektrode (209) gemeinsam genutzt wird.
20. Plasmaanzeigeanordnung (10) nach Anspruch 11, bei welcher der erste Spalt (210), zwischen den ersten und zweiten Elektroden (207, 208), der nicht

für die Dauerentladung ausgewählt wird, zweimal so groß ist wie der zweite Spalt (211), zwischen den ersten und zweiten Elektroden (207, 208), der für die Dauerentladung ausgewählt wird.

21. Plasmaanzeigeanordnung (10) nach Anspruch 11, wobei die erste Elektrodenbetriebseinrichtung umfasst:

einen Scan-Treiber, der in jeder der ersten Elektroden (207) so vorgesehen ist, dass er das Auswahlpotential und das Nicht-Auswahlpotential diesen zuführt; und einen gemeinsamen Treiber zum Zuführen eines Dauerimpulses (22a) zu den ersten Elektroden (207) gemeinsam, bei der Dauerentladung, die dem Adressenentladungsprozess folgt.

22. Plasmaanzeigeanordnung (10) nach Anspruch 21, bei welcher eine Energiequelle (29) für den Scan-Treiber (27) zum Betreiben der ersten Elektrode (207) auch eine Energie einem Adressentreiber (28) zum Betreiben der dritten Elektrode (209) zuführt.

23. Plasmaanzeigeanordnung (10) nach Anspruch 21, bei welcher der erste Spalt (210), zwischen den ersten und zweiten Elektroden (207, 208), der nicht für die Dauerentladung ausgewählt wird, das Zweifache der Breite des zweiten Spalts (211), zwischen den ersten und zweiten Elektroden (207, 208), der für die Dauerentladung ausgewählt wird, nicht überschreitet.

Revendications

1. Procédé de commande d'un panneau d'affichage à plasma pourvu de premières matrices d'électrodes disposées dans des rangées chacune formée d'une paire de première et seconde électrodes (207, 208), et de secondes matrices d'électrodes disposées dans des colonnes chacune formée d'une troisième électrode (209),

chacune des première et seconde matrices étant intercalées entre des substrats (205), des cellules d'affichage étant formées à des points d'intersection des électrodes des deux matrices, un traitement de décharge d'adresse pour écrire des informations est provoqué dans une cellule sélectionnée par l'application d'une impulsion à la seconde électrode (208) et à la troisième électrode (209) formant la cellule sélectionnée, et lesdites informations étant affichées pour qu'une décharge de soutien soit provoquée par l'application, selon les informations écrites comme un résultat du traitement de décharge d'adresse, d'une impulsion de soutien

aux première et seconde électrodes (207, 208) formant ladite cellule d'affichage, **caractérisé en ce que**

ledit traitement de décharge d'adresse est commandé pour qu'une différence de potentiel fournie par un potentiel de sélection pour la première électrode (207) et se produisant à travers un second espace qui réalise la décharge de soutien entre la paire de première et seconde électrodes (207, 208) d'une rangée soit supérieure à une différence de potentiel fournie par un potentiel de non-sélection pour la première électrode (207) et se produisant à travers un premier espace qui ne réalise pas la décharge de soutien entre la seconde électrode (208) d'une rangée et la première électrode (207) d'une autre rangée contiguë à la rangée.

2. Procédé de commande d'un panneau d'affichage à plasma selon la revendication 1, dans lequel ledit potentiel de non-sélection est commandé pour être inférieur à une tension de soutien minimum pour ledit premier espace (210), lorsque le traitement de décharge d'adresse est provoqué.

- 25 3. Procédé de commande d'un panneau d'affichage à plasma selon la revendication 1, dans lequel, dans une première moitié d'un traitement de décharge d'adresse, l'une des lignes d'affichage paires (201) et des lignes d'affichage impaires (201) sont sélectionnées pour l'affichage en sélectionnant celles correspondantes des secondes électrodes (208), et, dans une seconde moitié du traitement de décharge d'adresse, l'autre des lignes d'affichage paires (201) et des lignes d'affichage impaires (201) sont sélectionnées pour l'affichage, et

dans la première moitié du traitement de décharge d'adresse, les premières électrodes (207) pour l'une des lignes d'affichage paires (201) et des lignes d'affichage impaires (201) sont mises au potentiel de sélection et les premières électrodes (207) pour l'autre des lignes d'affichage paires (201) et des lignes d'affichage impaires (201) sont mises au potentiel de non-sélection, et est dans la seconde moitié du traitement de décharge d'adresse, les premières électrodes (207) pour l'autre des lignes d'affichage paires (201) et des lignes d'affichage impaires (201) sont mises au potentiel de sélection et les premières électrodes (207) pour l'une des lignes d'affichage paires (201) et des lignes d'affichage impaires (201) sont mises au potentiel de non-sélection.

4. Procédé de commande d'un panneau d'affichage à plasma selon la revendication 1, dans lequel une impulsion pour le potentiel de sélection appliquée à la première électrode (207) est positive par rapport à un potentiel de masse, une impulsion appliquée à la seconde électrode (208) pour la ligne d'afficha-

- ge sélectionnée est négative par rapport au potentiel de masse, et une impulsion appliquée à la troisième électrode (209) pour la cellule d'affichage sélectionnée est positive par rapport au potentiel de masse.
5. Procédé de commande d'un panneau d'affichage à plasma selon la revendication 4, dans lequel une tension pour le potentiel de sélection appliquée à la première électrode (207) est égale à une tension appliquée à la troisième électrode (209) pour la cellule d'affichage sélectionnée.
10. Procédé de commande d'un panneau d'affichage à plasma selon la revendication 4, dans lequel une tension pour le potentiel de non-sélection appliquée à la première électrode (207) est égale à une tension appliquée à la troisième électrode (209) pour la cellule d'affichage non sélectionnée.
15. Procédé de commande d'un panneau d'affichage à plasma selon la revendication 4, dans lequel la tension pour le potentiel de non-sélection appliquée à la première électrode (207) et la tension appliquée à la troisième électrode (209) pour la cellule d'affichage non sélectionnée sont mises au potentiel de masse.
20. Procédé de commande d'un panneau d'affichage à plasma selon la revendication 4, dans lequel la différence de potentiel de sélection ayant approximativement la moitié de l'amplitude d'une différence de potentiel entre la première électrode (207) et la seconde électrode (208) appliquée dans le traitement de décharge d'adresse est appliquée à la première électrode (207) et une impulsion de balayage (21) ayant approximativement la moitié de l'amplitude de ladite différence de potentiel de sélection est appliquée à la seconde électrode (208) formant la cellule sélectionnée.
25. Procédé de commande d'un panneau d'affichage à plasma selon la revendication 4, dans lequel des tensions pour un potentiel de non-sélection appliqués à la première électrode (207), à la seconde électrode (208) et à la troisième électrode (209) dans le traitement de décharge d'adresse sont mises au potentiel de masse.
30. Procédé de commande d'un panneau d'affichage à plasma selon la revendication 4, dans lequel ledit potentiel de sélection est appliqué à la première électrode (207) en synchronisme avec une application d'une impulsion de balayage (21) à la seconde électrode (208), lorsque les lignes d'affichage (201) sont sélectionnées séquentiellement une par une dans le traitement de décharge d'adresse.
35. Dispositif d'affichage à plasma (10) pourvu d'un panneau d'affichage à plasma, de premières matrices d'électrodes disposées dans des rangées chacune formée d'une paire de première et seconde électrodes (207, 208), et de secondes matrices d'électrodes disposées dans des colonnes chacune formée d'une troisième électrode (209),
40. chacune des première et seconde matrices étant intercalées entre des substrats (205), des cellules d'affichage étant formées à des points d'intersection des électrodes des deux matrices, un traitement de décharge d'adresse pour écrire des informations est provoqué dans une cellule sélectionnée par l'application d'une impulsion à la seconde électrode (208) et à la troisième électrode (209) formant la cellule sélectionnée, et lesdites informations étant affichées pour qu'une décharge de soutien soit provoquée par l'application, selon les informations écrites comme un résultat du traitement de décharge d'adresse, d'une impulsion de soutien aux première et seconde électrodes (207, 208) formant ladite cellule d'affichage,
45. ledit dispositif d'affichage à plasma (10) étant caractérisé en ce qu'il comprend :
- un moyen de commande de première électrode pour commander ledit traitement de décharge d'adresse pour qu'une différence de potentiel fournie par un potentiel de sélection pour la première électrode (207) et se produisant à travers un second espace qui réalise la décharge de soutien entre la paire de première et seconde électrodes (207, 208) d'une rangée soit supérieure à une différence de potentiel fournie par un potentiel de non-sélection pour la première électrode (207) et se produisant à travers un premier espace qui ne réalise pas la décharge de soutien entre la seconde électrode (208) d'une rangée et la première électrode (207) d'une autre rangée contigüe à la rangée.
50. 12. Dispositif d'affichage à plasma selon la revendication 11, dans lequel :
- ledit premier espace est plus large que ledit second espace, et
- ledit moyen de commande de première électrode comprenant :
- un premier dispositif de commande de sélection (23) pour commander, dans une première moitié du traitement de décharge d'adresse, les premières électrodes (207) pour l'une des lignes d'affichage paires (201) et des lignes d'affichage impaires (201) en appliquant aux premières électrodes (207) l'un du potentiel de sélection et du potentiel de non-sélection, dans une se-

- conde moitié du traitement de décharge d'adresse, les premières électrodes (207) pour l'une des lignes d'affichage paires (201) et des lignes d'affichage impaires (201) en appliquant aux premières électrodes (207) l'autre du potentiel de sélection et du potentiel de non-sélection ;

un second dispositif de commande de sélection (24) pour commander, dans une première moitié du traitement de décharge d'adresse, les premières électrodes (207) pour l'autre des lignes d'affichage paires (201) et des lignes d'affichage impaires (201) en appliquant aux premières électrodes (207) l'autre du potentiel de sélection et du potentiel de non-sélection, dans une seconde moitié du traitement de décharge d'adresse, les premières électrodes (207) pour l'autre des lignes d'affichage paires (201) et des lignes d'affichage impaires (201) en appliquant aux premières électrodes (207) l'un du potentiel de sélection et du potentiel de non-sélection ; et

un dispositif de commande commun (22) pour appliquer une impulsion de soutien aux premières électrodes (207) communément, dans la décharge de soutien suivant le traitement de décharge d'adresse.

13. Dispositif d'affichage à plasma (10) selon la revendication 11, dans lequel chacun dudit premier dispositif de commande de sélection (23) et du second dispositif de commande de sélection (24) comprend des éléments de commutation (25).

14. Dispositif d'affichage à plasma (10) selon la revendication 13, dans lequel chacun dudit premier dispositif de commande de sélection (23) et du second dispositif de commande de sélection (24) comprend :

un premier élément de sélection (25) pour appliquer ledit potentiel de sélection à la première électrode (207) ;

un second élément de commutation (25) pour commuter un potentiel de la première électrode (207) audit potentiel de sélection ;

un troisième élément de commutation (25) pour fixer un potentiel de la première électrode (207) audit potentiel de non-sélection.

15. Dispositif d'affichage à plasma (10) selon la revendication 11, dans lequel ledit dispositif de commande commun (22) pour appliquer ladite impulsion de soutien (22a) à ladite première électrode (207) possède un chemin d'absorption de courant et un chemin de fourniture de courant qui sont séparés l'un de l'autre, chacun dudit chemin d'absorption de courant et dudit chemin de fourniture de courant étant raccordé audit premier dispositif de commande de sélection (23), au second dispositif de commande de sélection (24) et aux premières électrodes (207).

16. Dispositif d'affichage à plasma (10) selon la revendication 15, dans lequel des première et seconde diodes (26) sont raccordées audit chemin de fourniture de courant dudit dispositif de commande commun (22) dans un dispositif ayant par rapport aux premières électrodes (207), des troisième et quatrième diodes (26) sont raccordées audit chemin d'absorption de courant dans une disposition inverse par rapport aux premières électrodes (207), un premier de circuit de sélection est raccordé à un point de connexion entre lesdites première et troisième diodes (26), et les premières électrodes (207), et un second circuit de sélection est raccordé à un point de connexion entre lesdites seconde et quatrième diodes (26), et les premières électrodes (207).

17. Dispositif d'affichage à plasma (10) selon la revendication 11, dans lequel une tension inférieure à une tension appliquée à la première électrode (207) adjacente au second espace (211) est appliquée, en utilisant le dispositif de commande de sélection, à la première électrode (207) adjacente au premier espace (210) et adjacente à la seconde électrode (208) et sélectionnée pour le traitement de décharge d'adresse.

18. Dispositif d'affichage à plasma (10) selon la revendication 11, dans lequel, dans une première moitié du traitement de décharge d'adresse, ledit potentiel de sélection est appliqué à un groupe de premières électrodes (207) pour l'une des lignes d'affichage paires (201) et des lignes d'affichage impaires (201), en utilisant l'un du premier dispositif de commande de sélection et du second dispositif de commande de sélection, et ledit potentiel de non-sélection est appliqué à l'autre groupe de premières électrodes (207) pour l'autre des lignes d'affichage paires (201) et des lignes d'affichage impaires (201), le traitement de décharge d'adresse est séquentiellement exécuté dans un groupe de premières électrodes (207), après quoi le potentiel de non-sélection est appliqué à un groupe de premières électrodes (207), et le potentiel de sélection est appliqué à l'autre groupe de premières électrodes (207) pour que le traitement de décharge d'adresse soit exécuté dans l'autre du groupe des premières électrodes (207), et ladite impulsion de soutien (22a) est appliquée audit dispositif de commande commun de toutes les premières électrodes (207) dans le dispositif d'affichage à plasma (10) pour que la décharge de

soutien soit provoquée pour un affichage illuminé dans le dispositif d'affichage à plasma (10).

19. Dispositif d'affichage à plasma (10) selon la revendication 11, dans lequel une source d'alimentation (29) pour le premier dispositif de commande de sélection (23) et le second dispositif de commande de sélection (24) est partagée par un dispositif de commande d'adresse (28) pour commander ladite troisième électrode (209). 5
20. Dispositif d'affichage à plasma (10) selon la revendication 11, dans lequel ledit premier espace (210), entre lesdites première et seconde électrodes (207, 208), non-sélectionné pour la décharge de soutien est deux fois aussi grand que le second espace (211), entre les première et seconde électrodes (207, 208), sélectionné pour la décharge de soutien. 15
21. Dispositif d'affichage à plasma (10) selon la revendication 11,
ledit moyen de commande de première électrode comprenant : 20
- un dispositif de commande de balayage placé dans chacune des premières électrodes (207) afin d'appliquer le potentiel de sélection et le potentiel de non-sélection à celles-ci ; et 25
- un dispositif de commande commun pour appliquer une impulsion de soutien (22a) aux premières électrodes (207) communément, dans la décharge de soutien après le traitement de décharge d'adresse. 30
22. Dispositif d'affichage à plasma (10) selon la revendication 21, dans lequel une source d'alimentation (29) pour le dispositif de commande de balayage (27) pour commander la première électrode (207) applique aussi une alimentation au dispositif de commande d'adresse (28) pour commander la troisième électrode (209). 40
23. Dispositif d'affichage à plasma (10) selon la revendication 21, dans lequel le premier espace (210), entre les première et seconde électrodes (207, 208), non-sélectionné pour la décharge de soutien ne dépasse pas deux fois la largeur du second espace (211), entre les première et seconde électrodes (207, 208), sélectionné pour la décharge de soutien. 45
- 50

FIG. 1
PRIOR ART

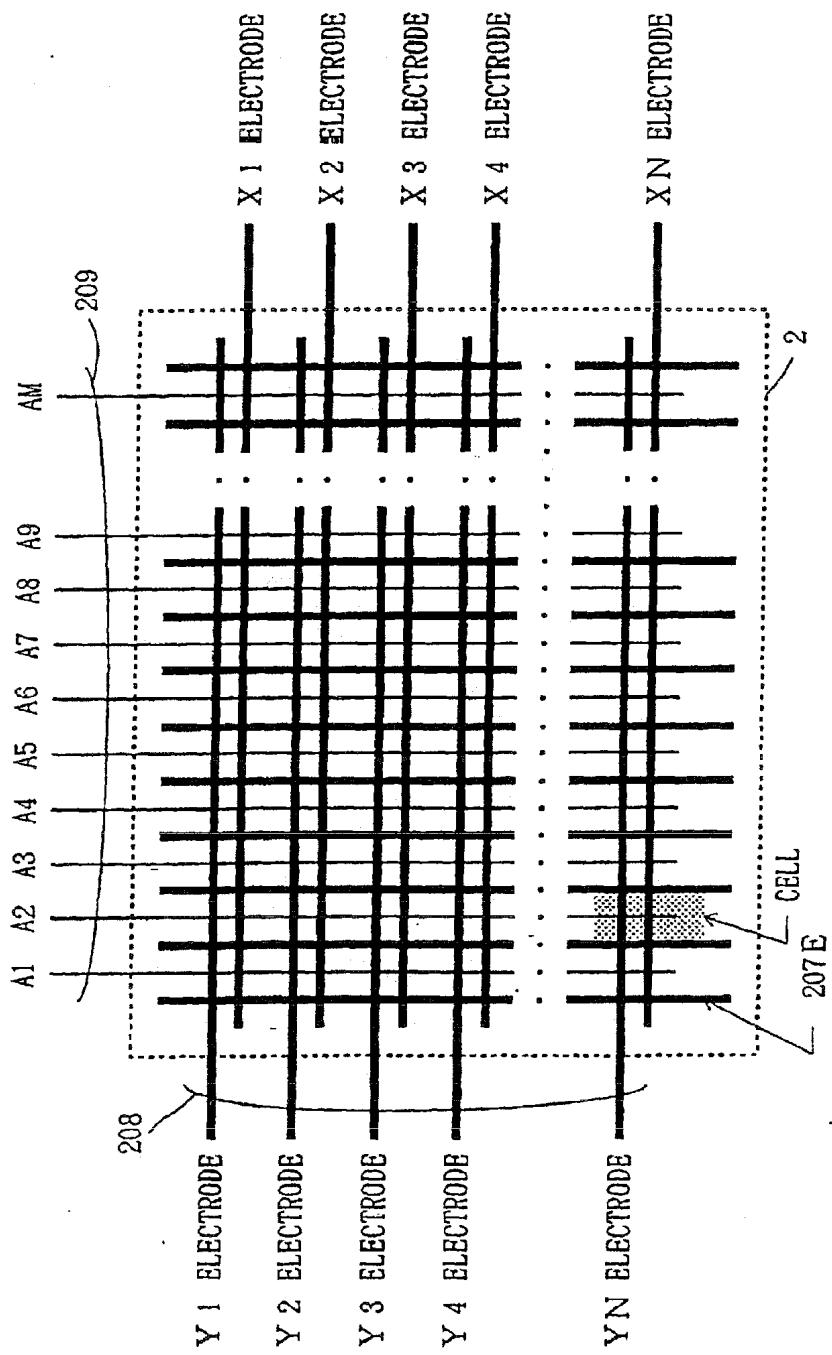


FIG. 2
PRIOR ART

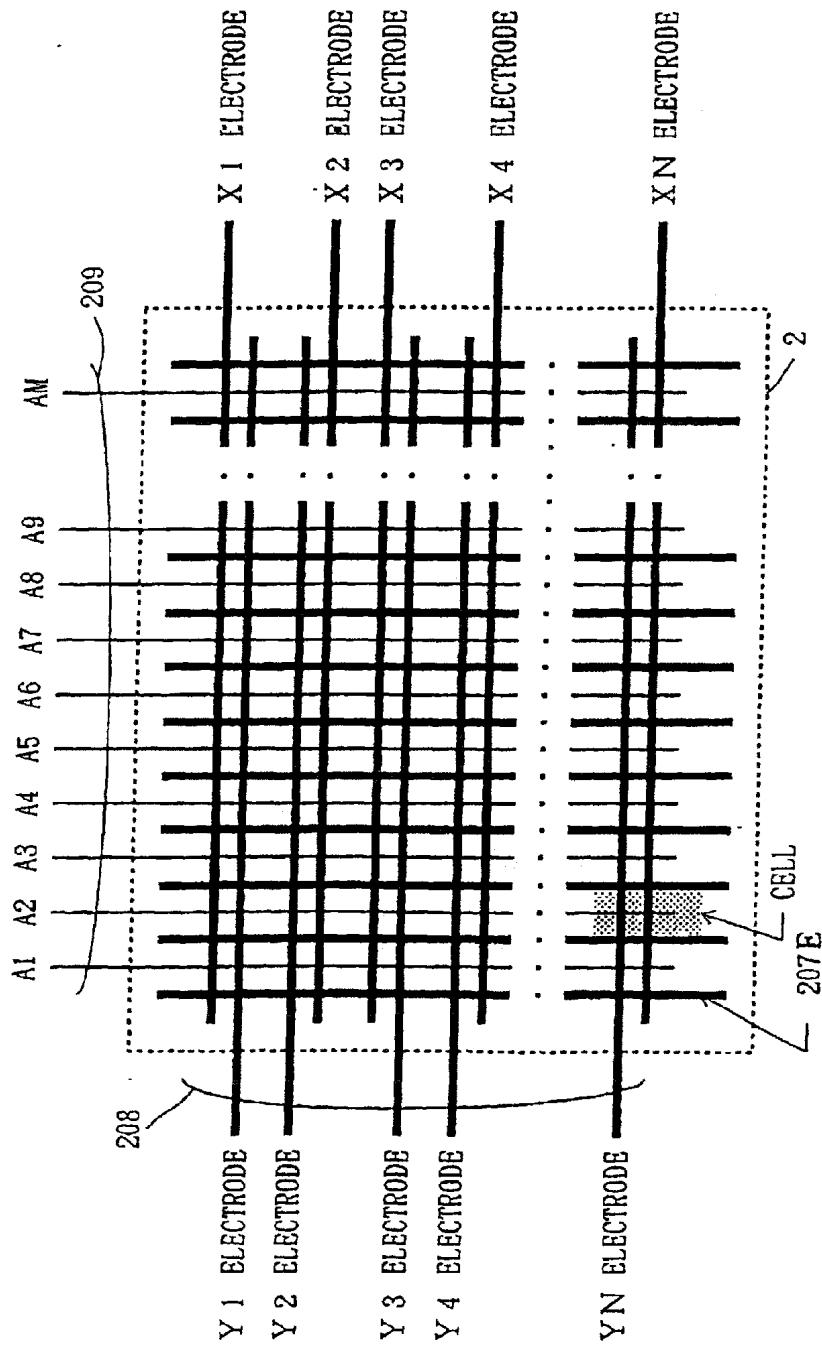


FIG. 3
PRIOR ART

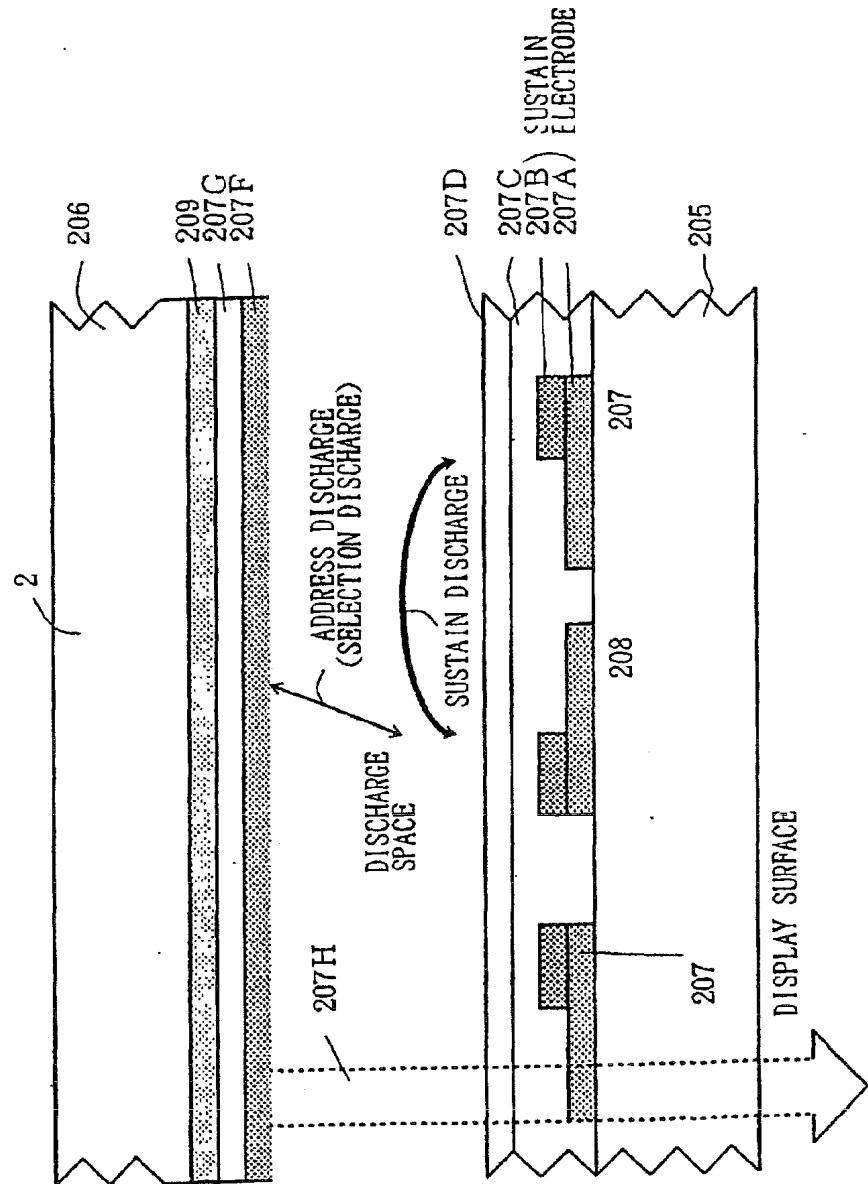


FIG. 4

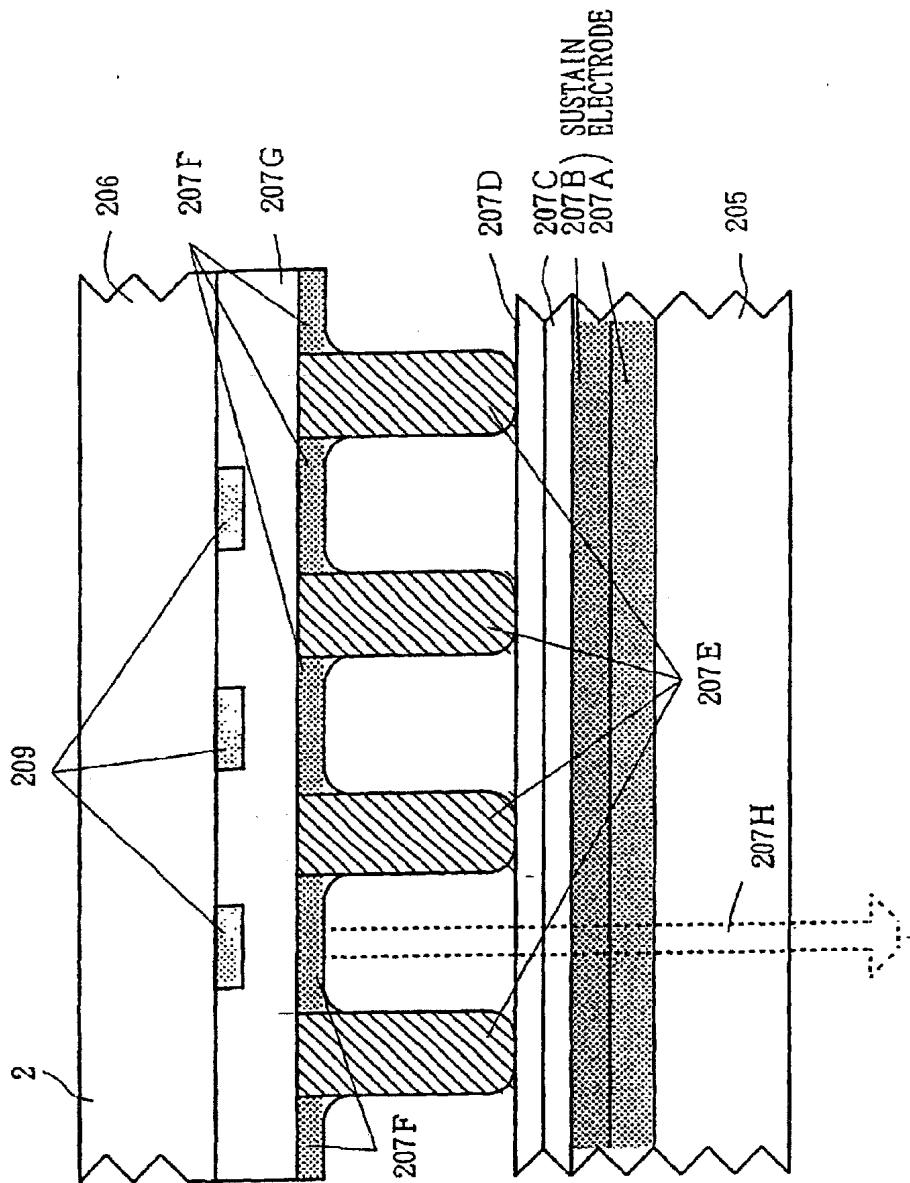


FIG. 5

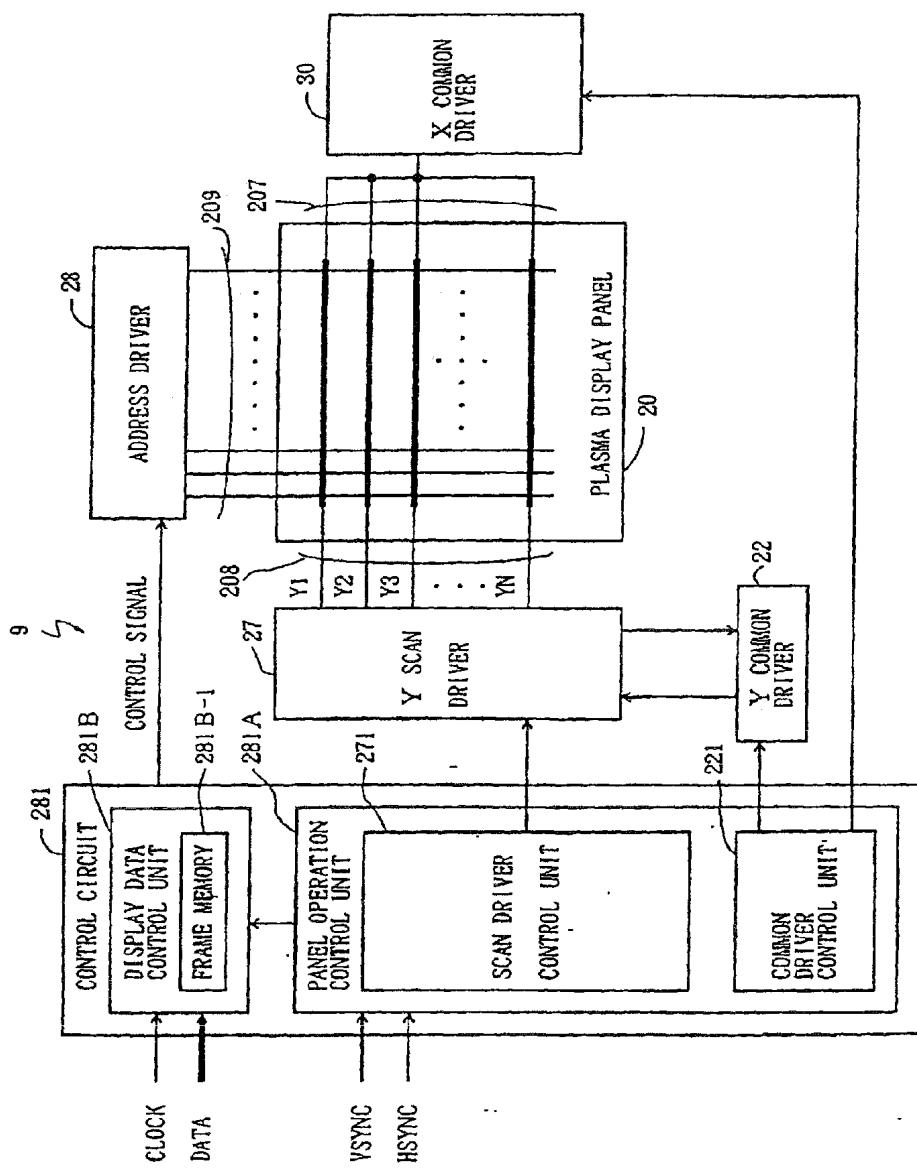


FIG. 6

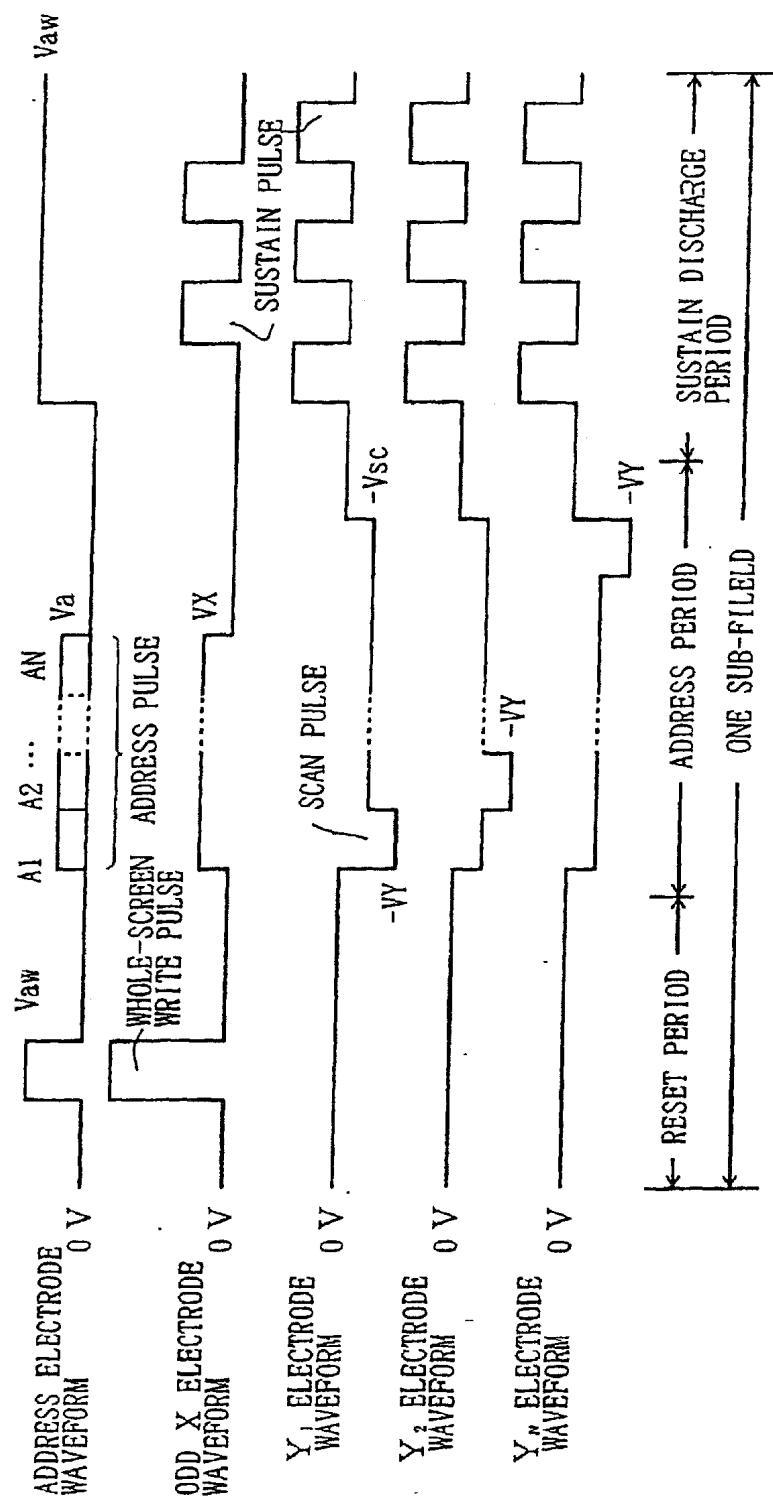


FIG. 7A

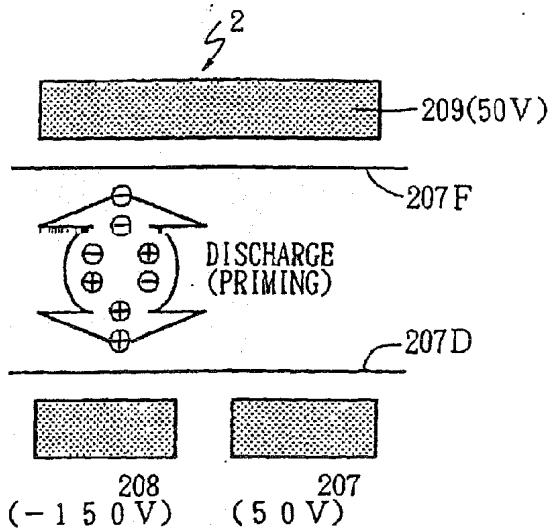


FIG. 7B

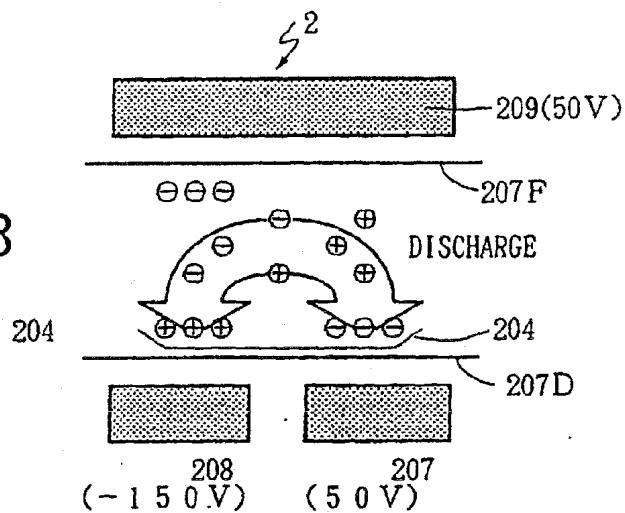


FIG. 7C

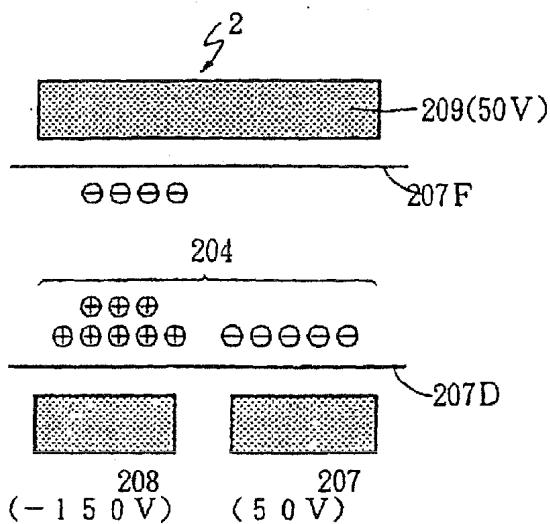
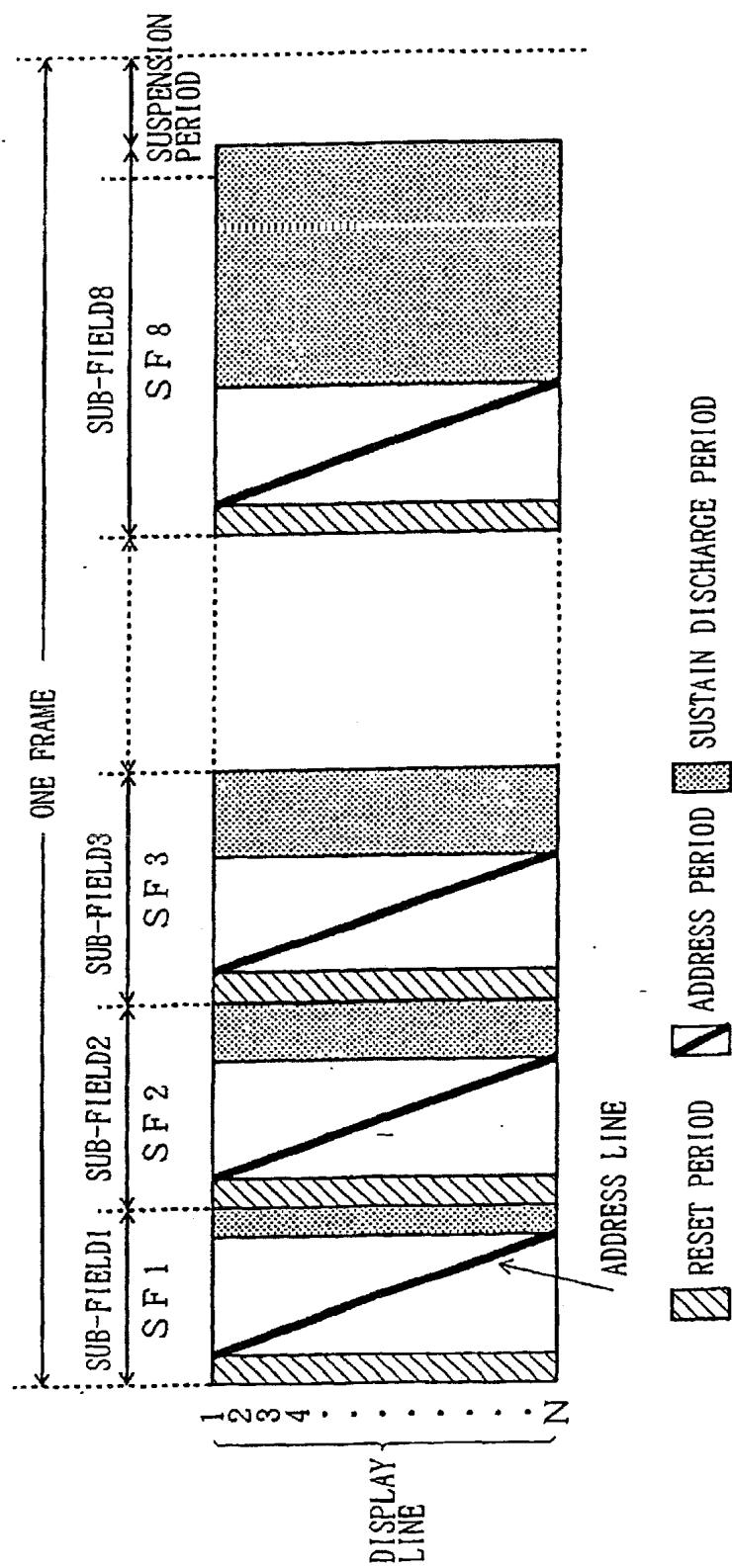


FIG. 8



၈

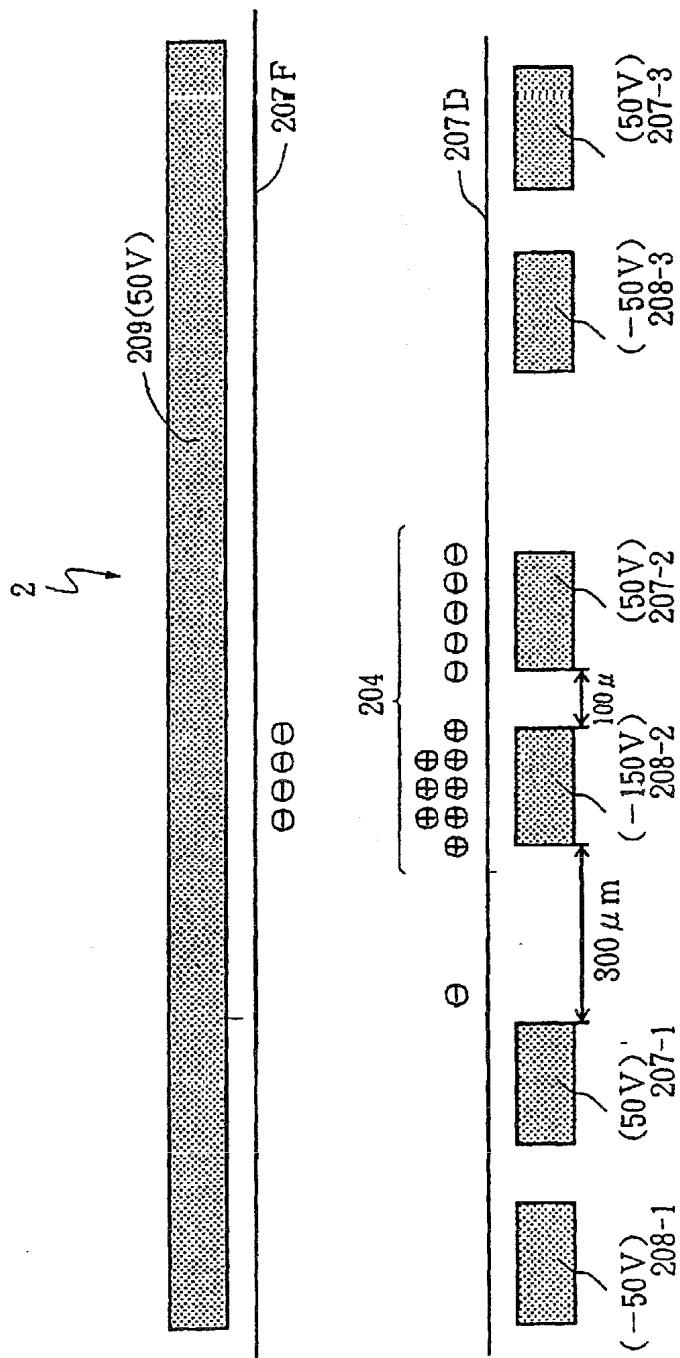


FIG. 10

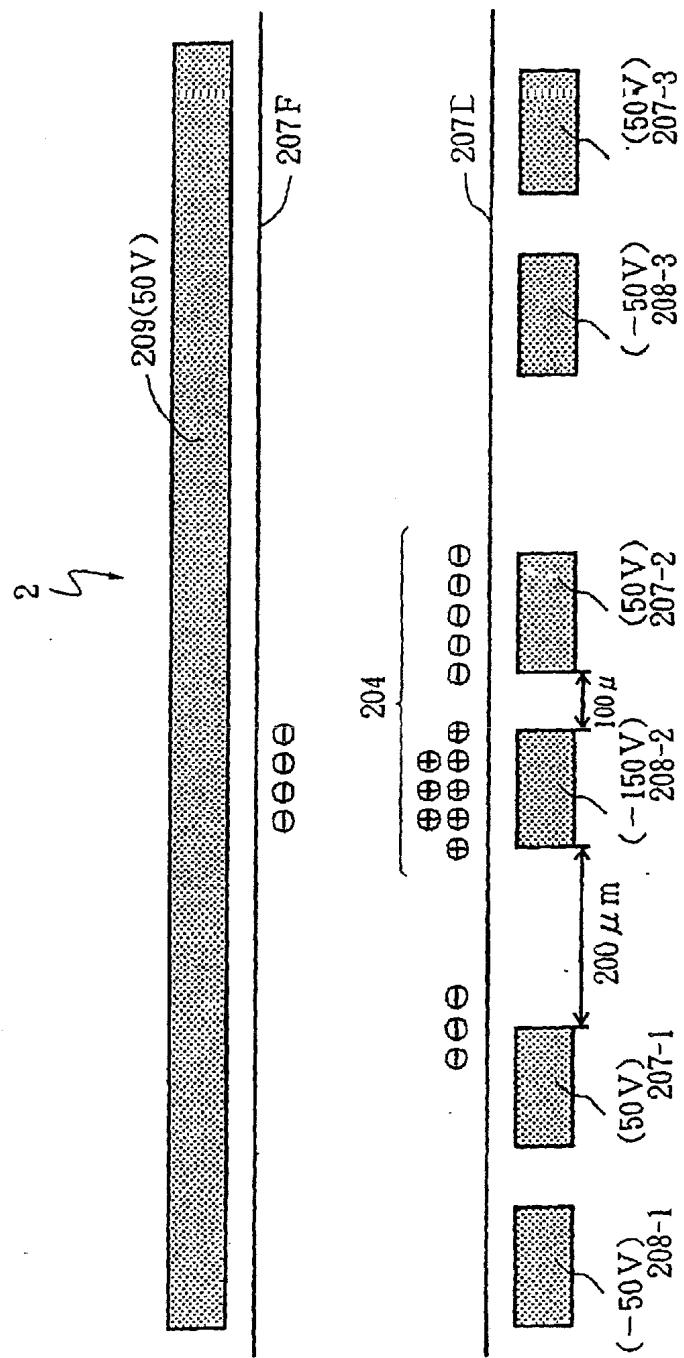


FIG. 11

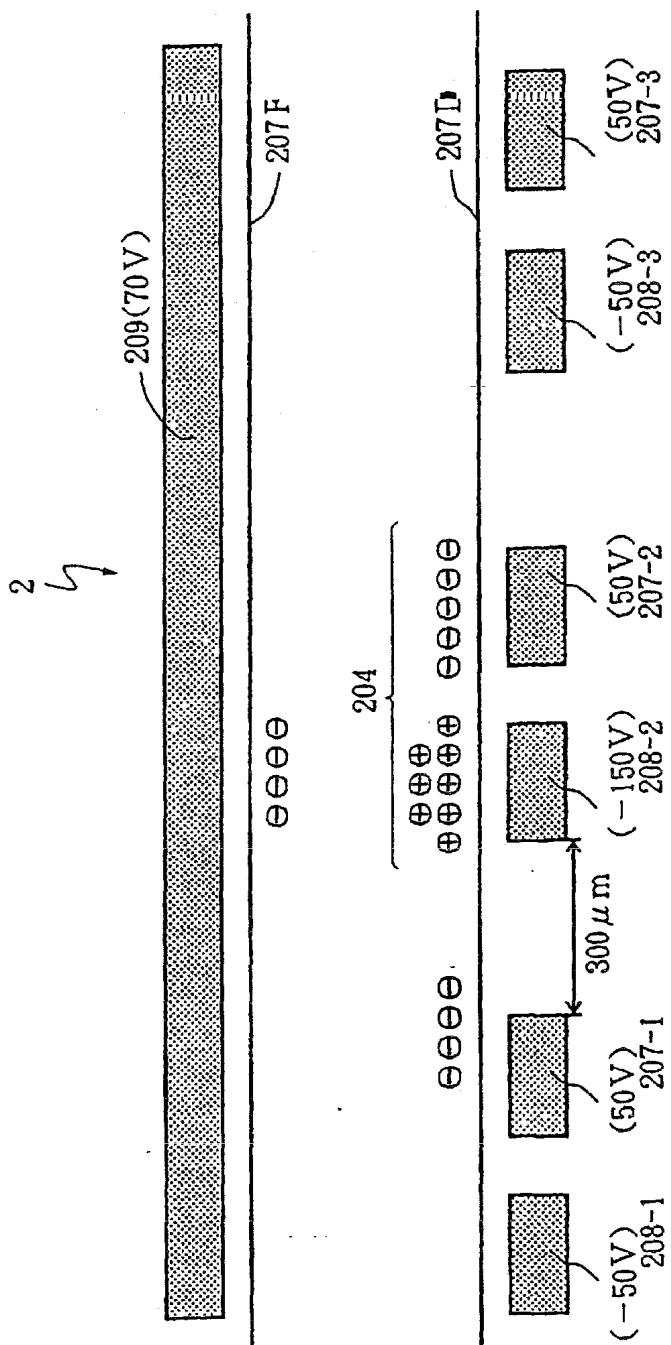


FIG. 12

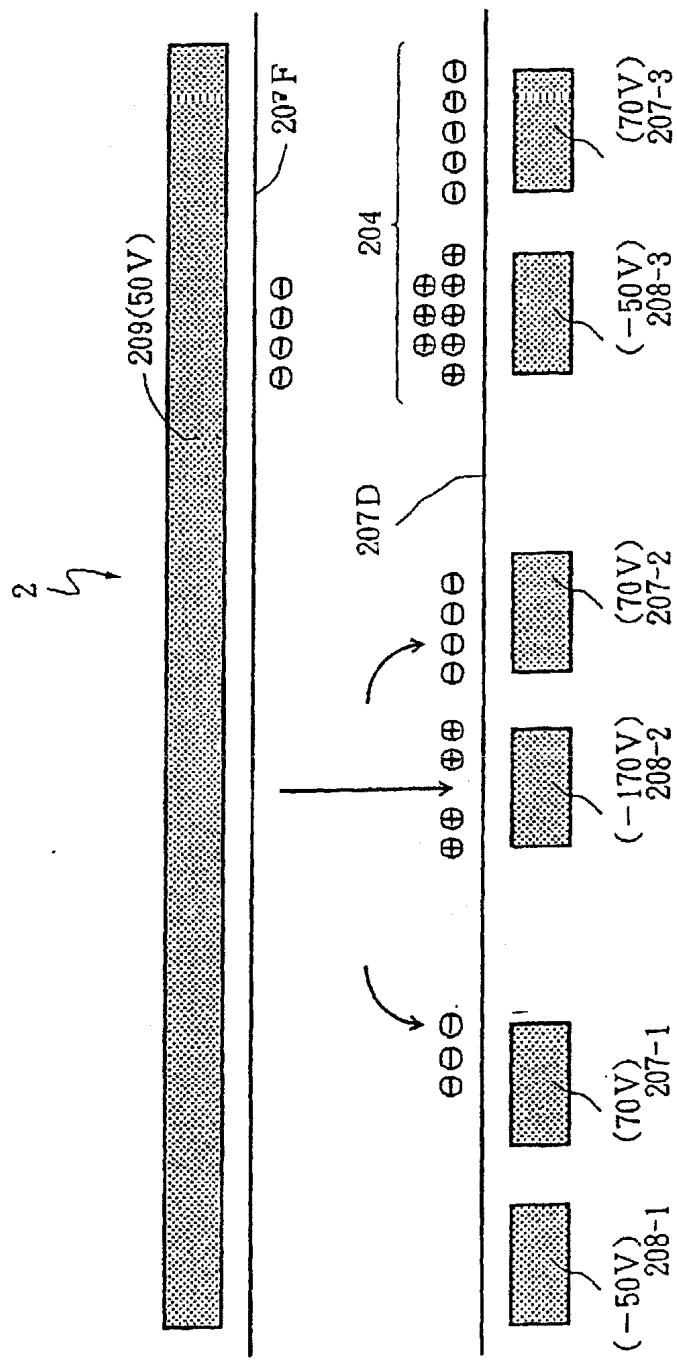


FIG. 13

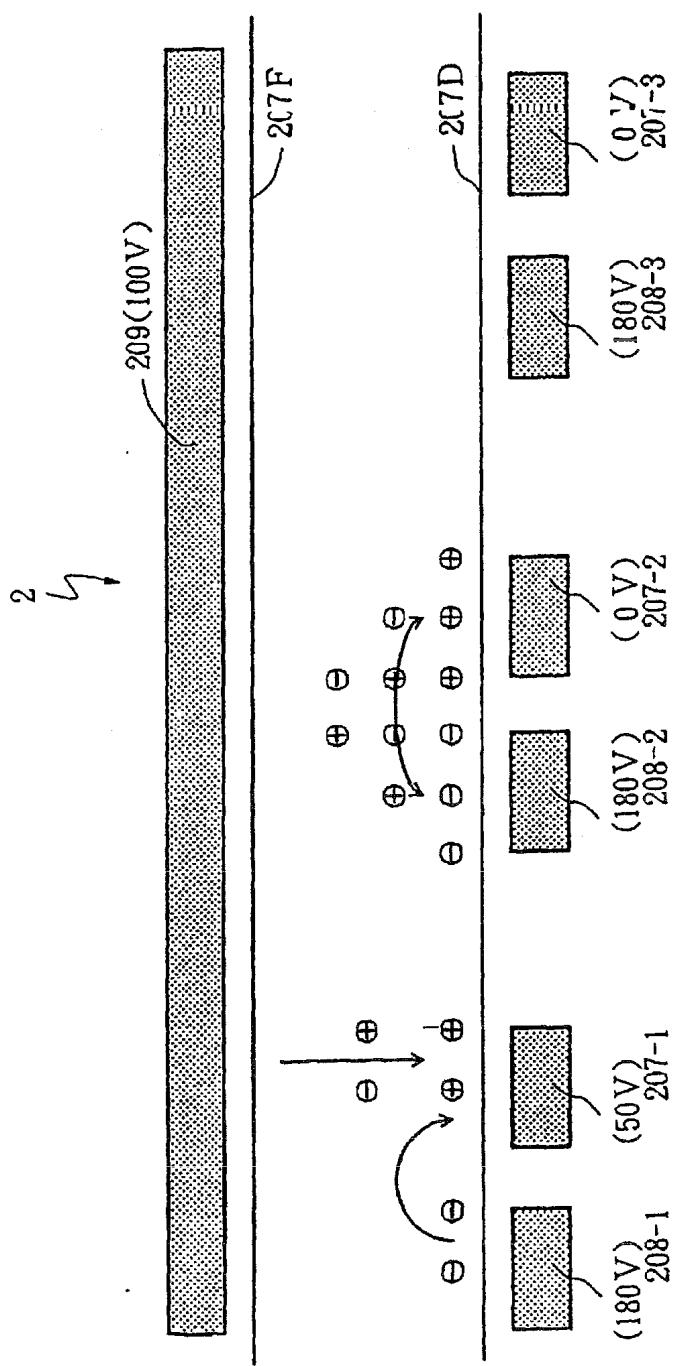


FIG. 14

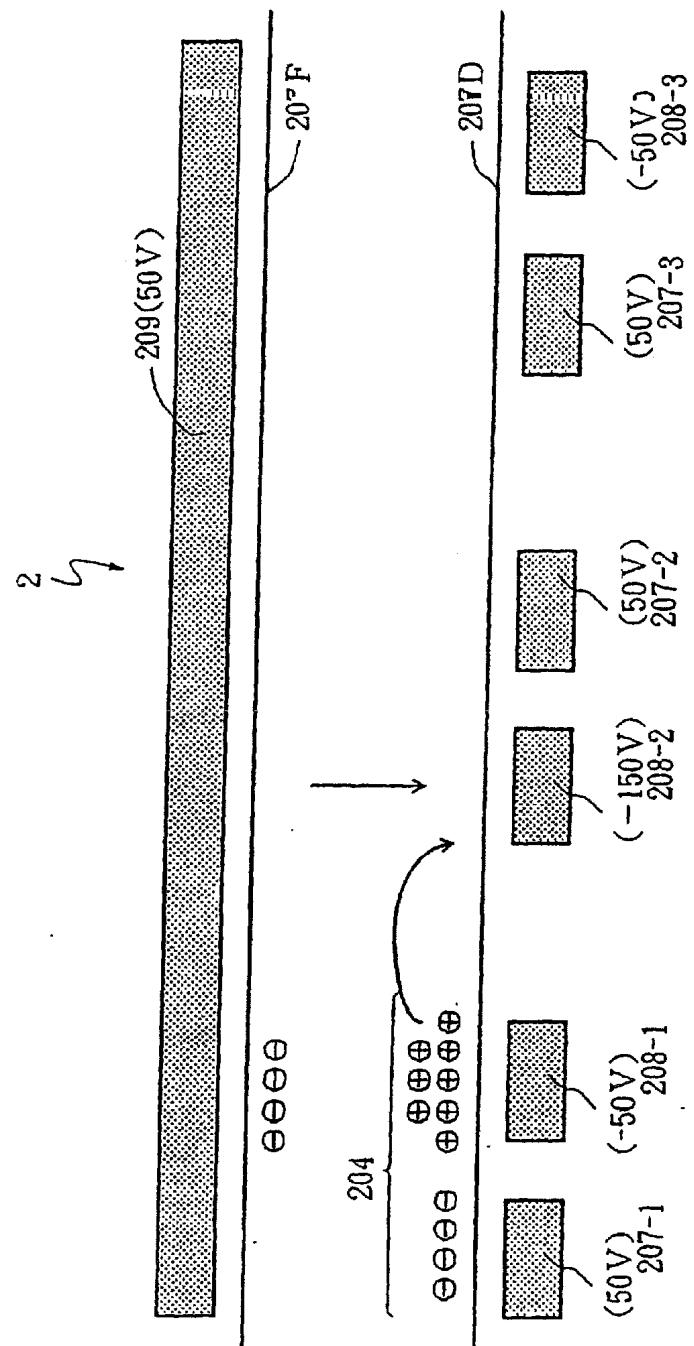


FIG. 15

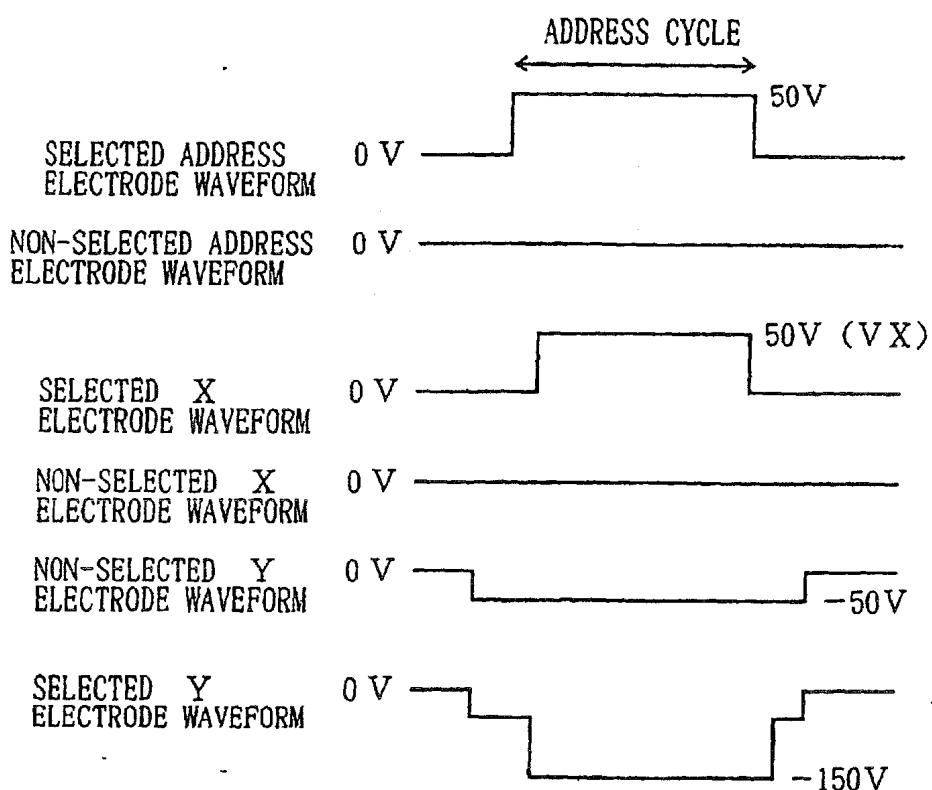


FIG. 16

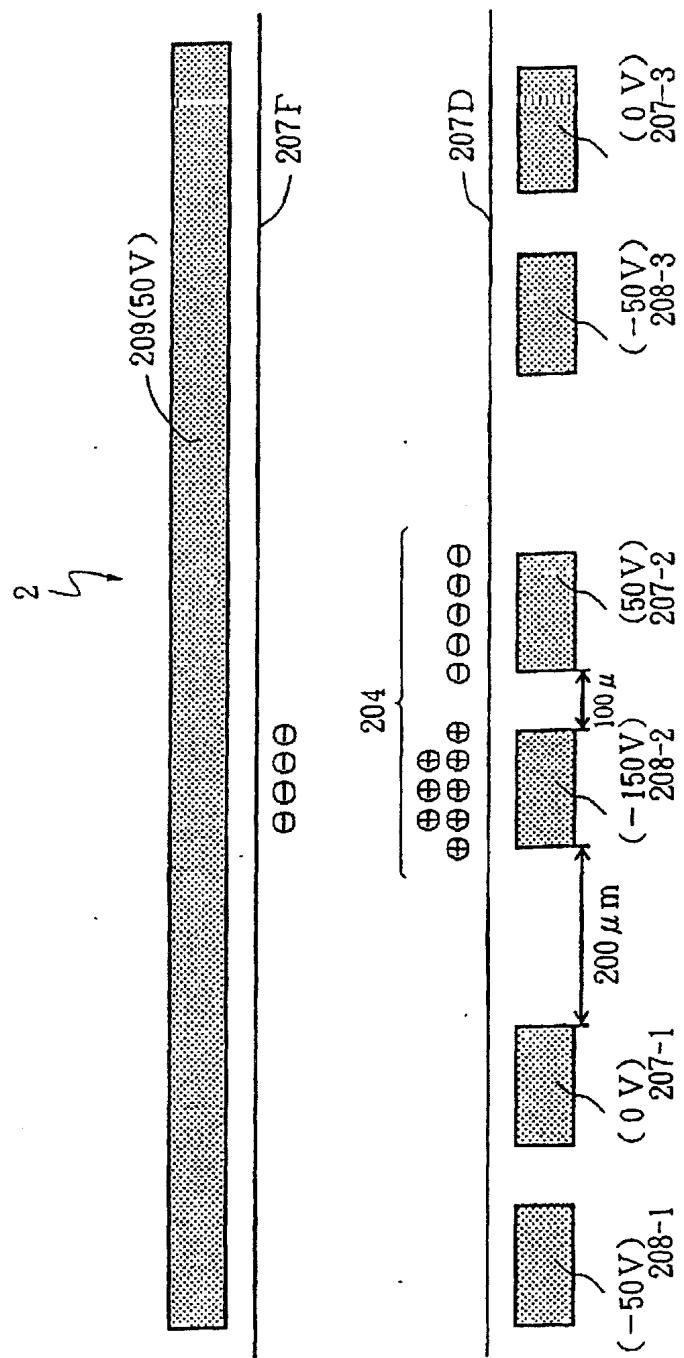


FIG. 17

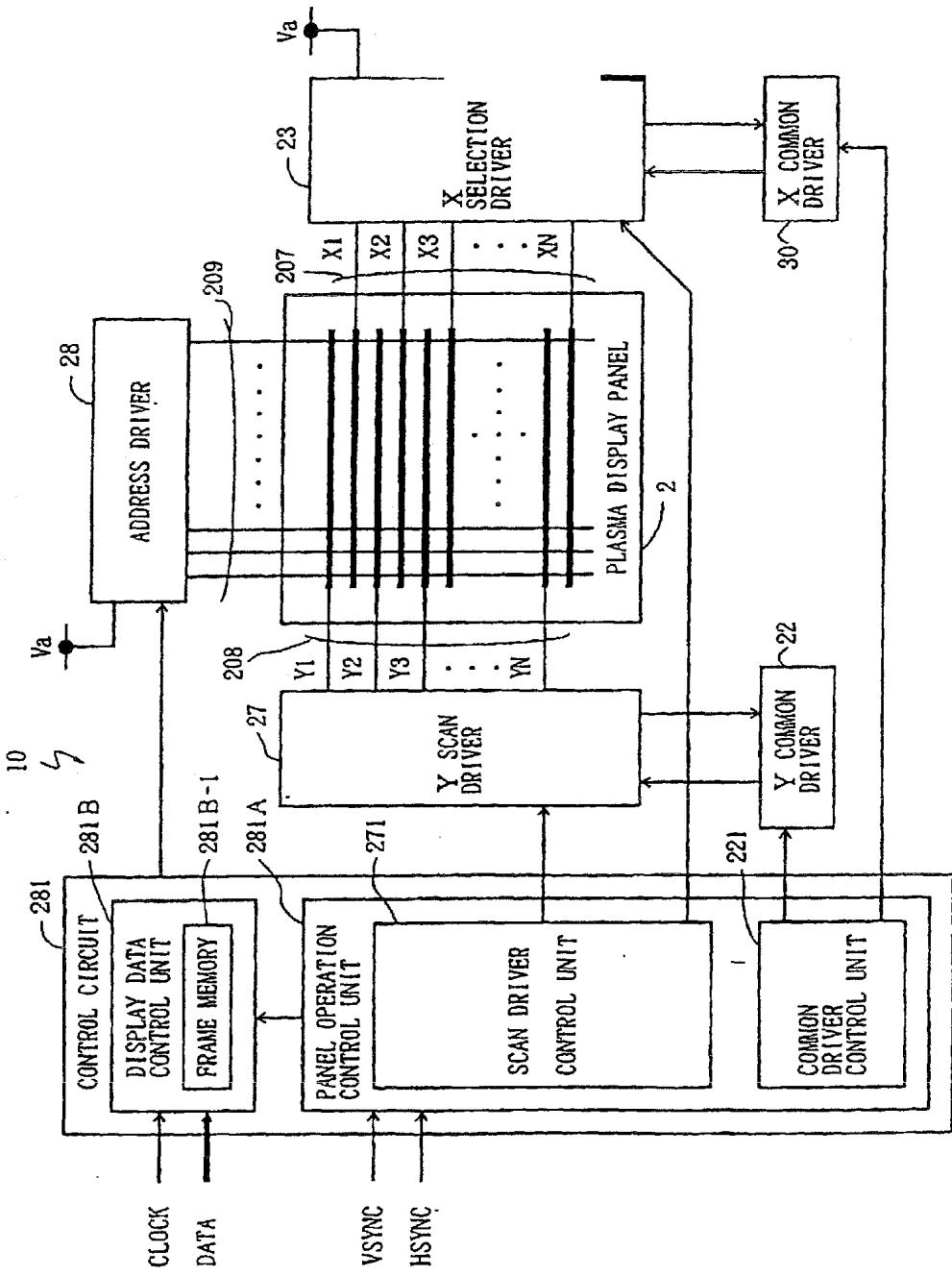


FIG. 18

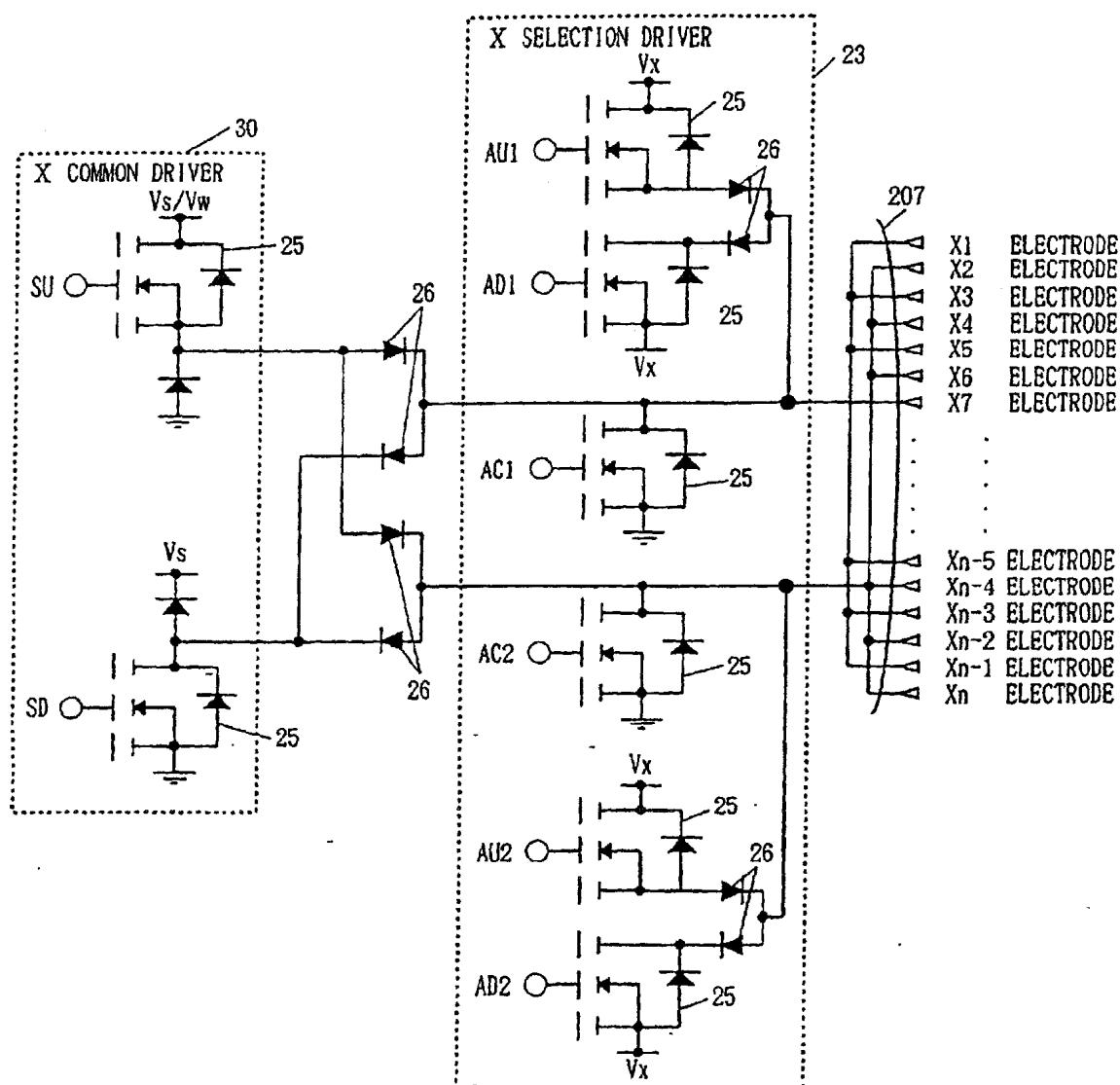


FIG. 19

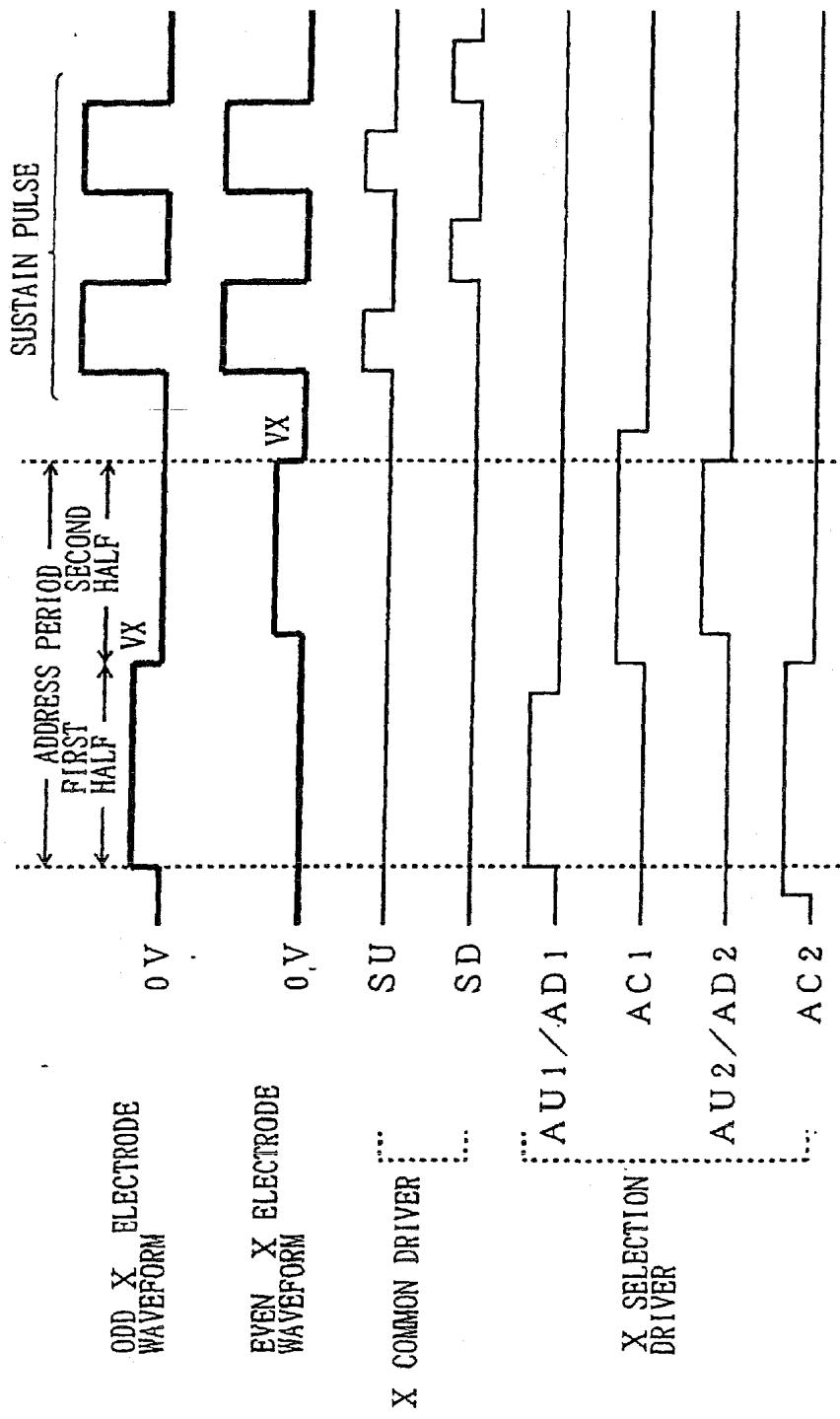


FIG. 20

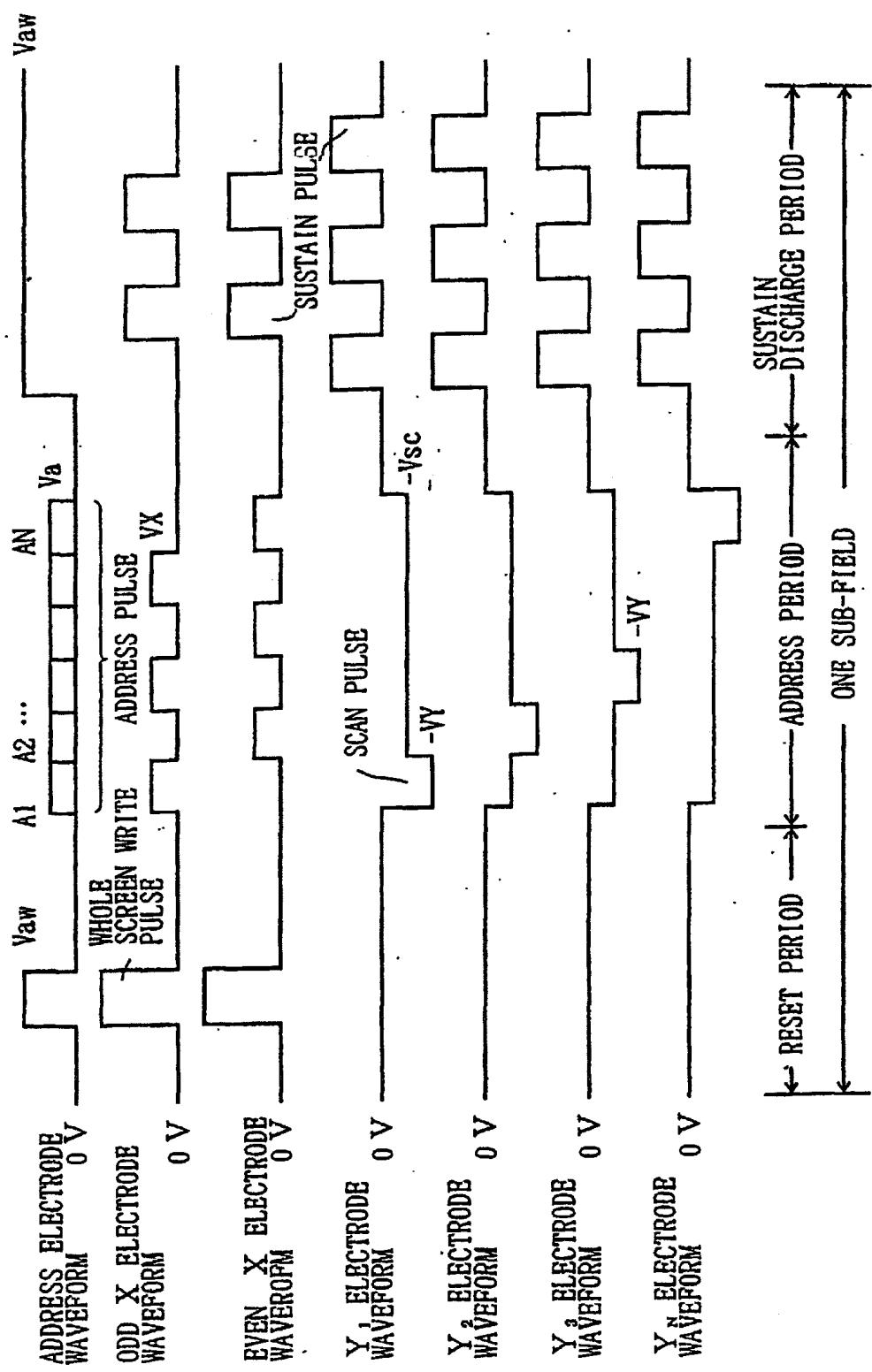


FIG. 21

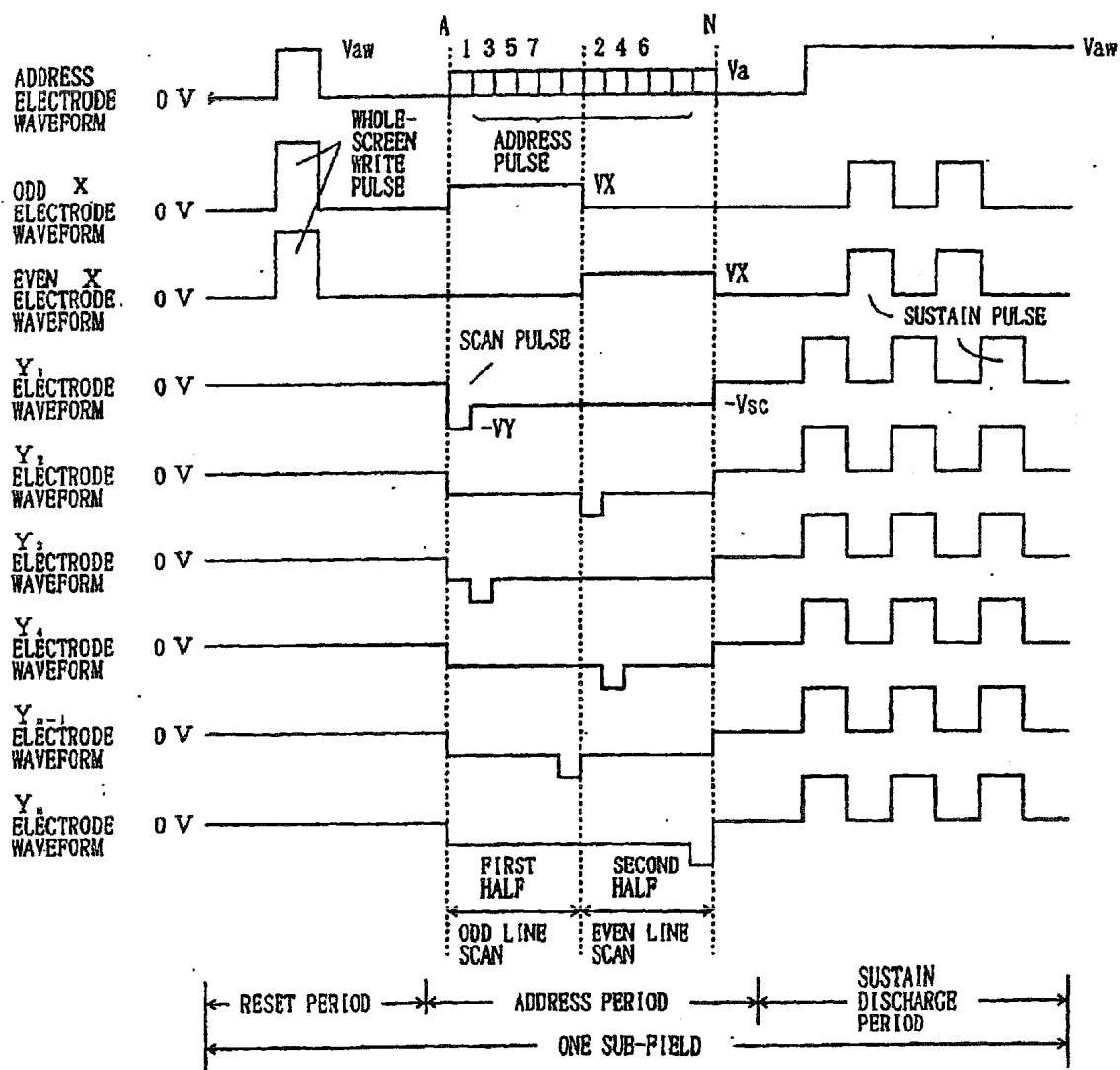


FIG. 22

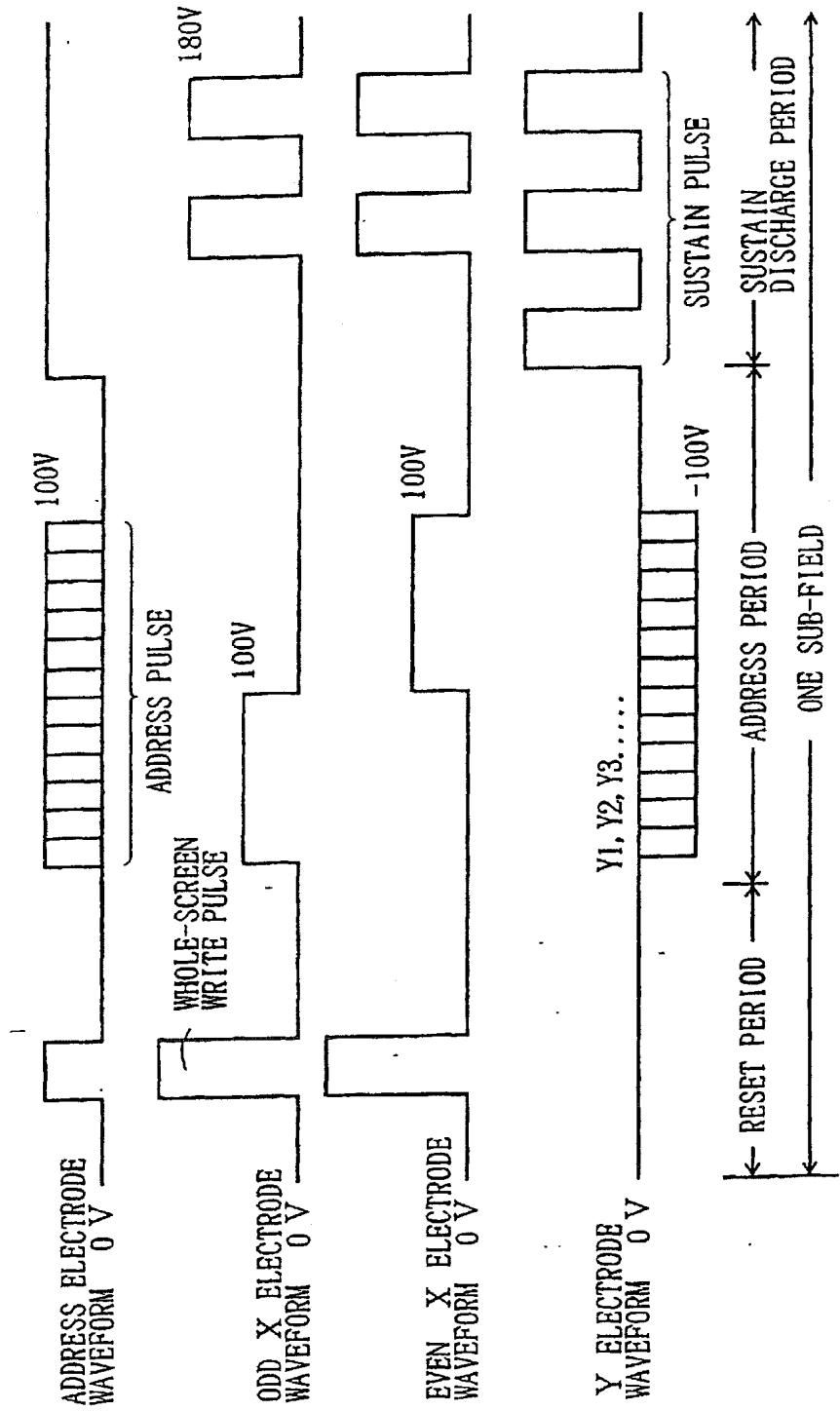


FIG. 23

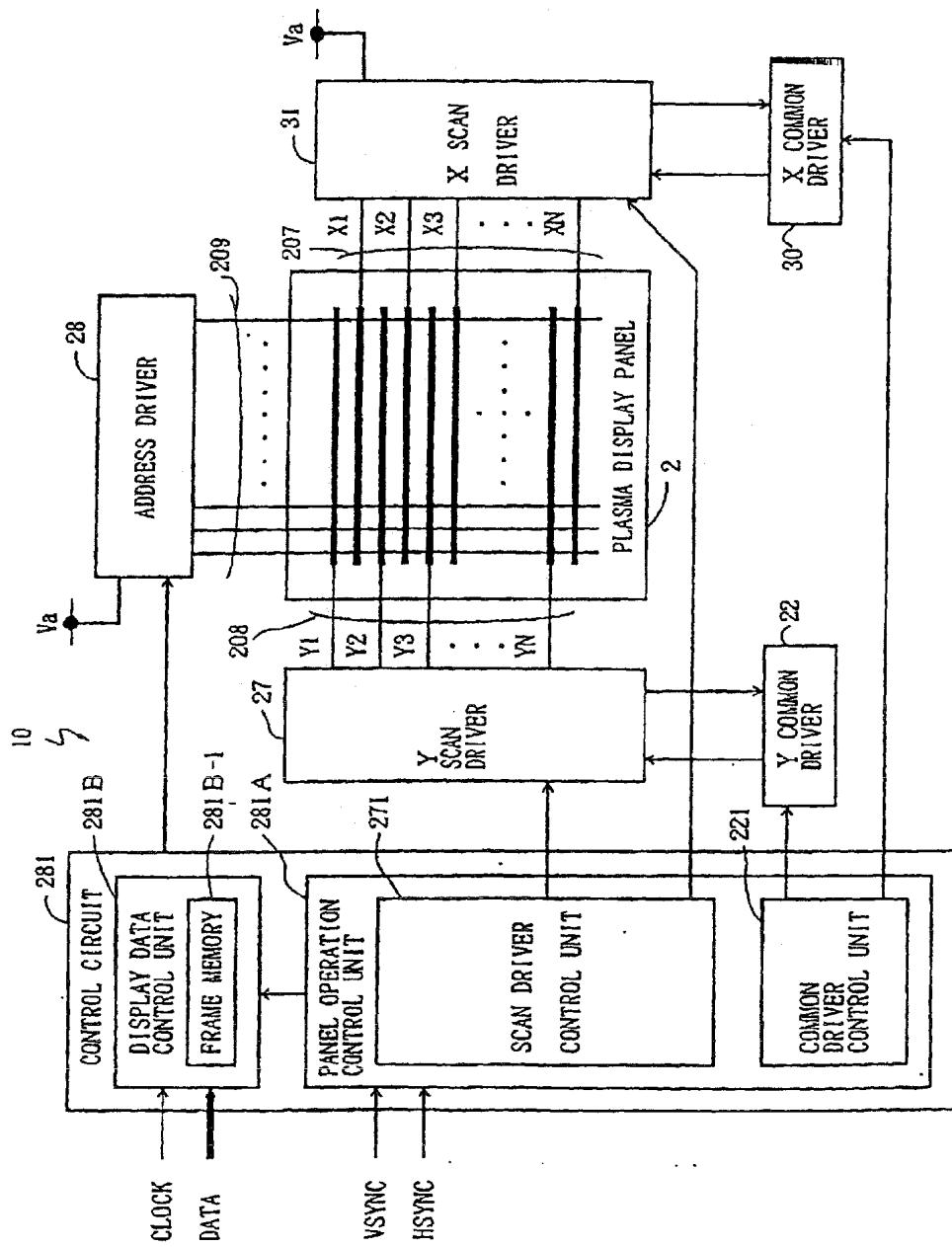


FIG. 24

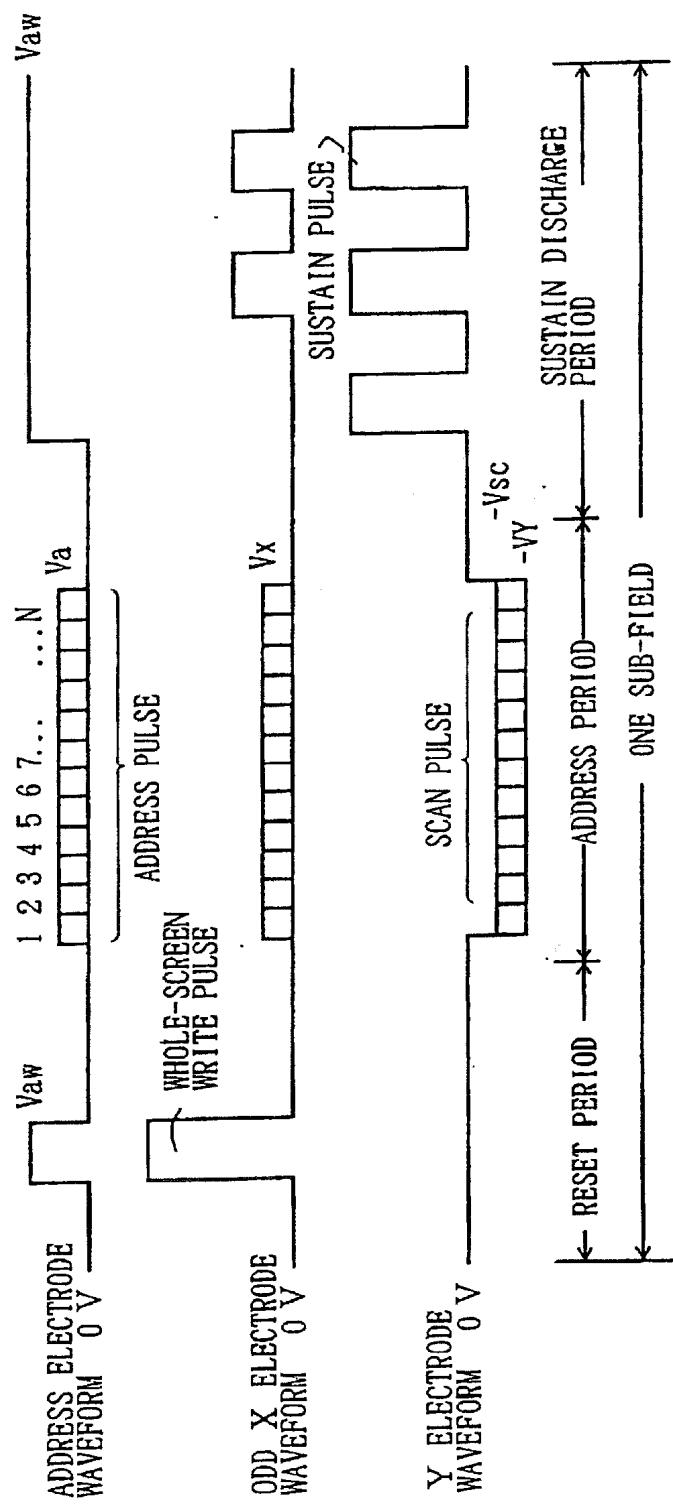


FIG. 25

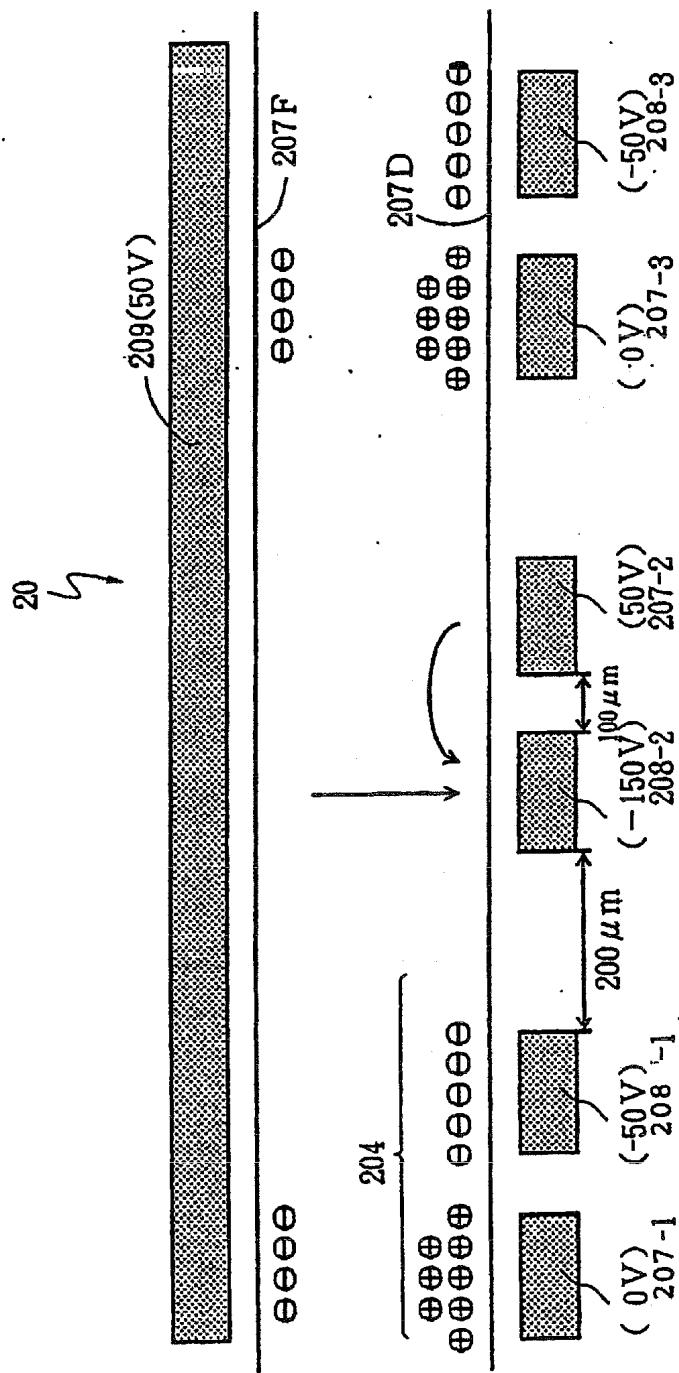


FIG. 26

